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Determinism \rightarrow
(Event structure isomorphism = Step sequence equivalence)

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Determinism \rightarrow

(Event Structure Isomorphism = Step Sequence Equivalence)

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A concurrent system S is called *deterministic* if for all states s of S we have that whenever S can evolve from state s into states s' and s'' by doing an action a , it must be the case that s' equals s'' . It is well known that for deterministic concurrent systems, most of the interleaved equivalences (bisimulation-, failure-, trace-equivalence) coincide. In this paper we prove in the setting of event structures that also most of the non-interleaved equivalences coincide (with each other) on this domain. In the last section of the paper we show that, as a consequence of our result, the causal structure of a deterministic concurrent system can be unravelled by observers who are capable to observe the beginning and termination of events.

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1980 Mathematical Subject Classification: 68B10.

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§1 INTRODUCTION

A (discrete) concurrent system generates events as it evolves in time. At any moment a set of events will have occurred and these will be ordered 'in time' or by 'causal precedence'. This order may be partial. When modelling concurrent systems and reasoning about their behaviour, it is often useful to consider different events as occurrences of the same action. This may indicate that certain events are produced by the same physical resource or that they cannot be distinguished by an observer. The relation between events and actions can be expressed by a labelling function $l: E \rightarrow A$ that relates an action to each event. Different approaches to the modelling of concurrent systems can be classified by looking at the types of labelling functions they allow for. For instance, if one models a concurrent system with an elementary net system [24], then it can never be the case that in some behaviour two events with the same label are concurrent (i.e. not related by the ordering). If we consider the usual semantics for process algebra languages like CCS [17], TCSP [14], ACP [4] and MEJE [3], then it turns out that these languages are very liberal wrt labellings of events: there is (almost) no restriction at all. There exists a very rich theory of 'comparative concurrency semantics' relating the interleaved semantics for CCS-like languages, i.e. those semantics which do not treat concurrency as a primitive notion. Now a well-known result says that almost all these equivalences (bisimulation equivalence, trace equivalence and everything in between) coincide for *deterministic* systems (see for instance ENGELFRIET [9]). A concurrent system S is called deterministic if for all states s of S we have that whenever S can evolve from state s into states s' and s'' by doing an action a , it must be the case that s' equals s'' .

Recently, many equivalences have been proposed that do consider concurrency as a primitive

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notion. Besides the event structure equivalence and the step sequence equivalence that will be discussed in this paper, we have for instance the occurrence net equivalence of NIELSEN, PLOTKIN & WINSKEL [18], the NMS equivalence of DEGANO, DE NICOLA & MONTENARI [8], the BS bisimulation of TRAKHTENBROT, RABINOVICH & HIRSHFELD [27], the step failure semantics of TAUBNER & VOGLER [26], the step bisimulation semantics of NIELSEN & THIAGARAJAN [19], the pomset semantics of PRATT [22], the pomset bisimulation semantics of BOUDOL & CASTELLANI [6], the generalised pomset bisimulation and the ST-bisimulation of VAN GLABBEEK & VAANDRAGER [11], the split sequence equivalence which we present at the end of this paper, etc, etc.

Now one can ask the obvious question what happens with all these equivalences if we restrict ourselves to the domain of deterministic systems. The main result of this paper is that almost all non-interleaved equivalences coincide (with each other) for deterministic systems. More specifically, we will show that step sequence equivalence and event structure isomorphism agree on this domain. Of the equivalences mentioned above only occurrence net equivalence is not situated in between step sequence equivalence and event structure isomorphism.

Event structures. A natural domain for modelling concurrency is the class of *event structures*, which were introduced in NIELSEN, PLOTKIN & WINSKEL [18]. By now many different types of event structures have been defined. For an overview we refer to WINSKEL [28]. In our view an especially important class of event structures is the class of *prime event structures*. Prime event structures contain no *junk*: every event in the set of events of a prime event structure can occur in at least one behaviour. The event structures used in this paper are labelled prime event structures with binary conflict. Below we give a formal definition of this type of event structures, followed by some explanatory remarks. The assumption of binary conflict is not essential in the proof of the main theorem of this paper. Because most people will be more familiar with event structures with binary conflicts and because the main use we foresee of our theorem lies in the field of CCS-like languages (where conflict is always binary), we decided to present the theorem for the case with binary conflict only, and to leave the generalisation to the case with arbitrary conflict as a (simple) exercise to the reader.

Arbitrary interleaving versus ‘True’ concurrency. In the last section of the paper some consequences will be discussed of our result for the issue of arbitrary interleaving versus ‘True’ concurrency. We introduce an operator which splits each event into a beginning and an end and show that the causal structure of a deterministic concurrent system can be unravelled by observers who are capable to observe these beginnings and ends.

Related work. One can view the main theorem of this paper as a *retrievability* result: given the step sequences of a deterministic event structure, we can retrieve this event structure up to isomorphism. Within the theory of concurrency there are quite a number of other retrievability results. BEST & DEVILLERS [5] prove various retrievability results for Petri nets. KIEHN [15] describes how the partial language of a p/t net can be recovered from the set of its step sequences. SHIELDS [25] considers a subclass of deterministic systems (‘behaviour systems with conservative labelling’) which makes it possible to lift concurrency up to a relation on labels, just like in MAZURKIEWICZ’s trace theory [16]. In both cases the partial order structure of a system can be retrieved from firing sequences (or words) and the concurrency relation. In TRAKHTENBROT, RABINOVICH & HIRSHFELD [27], some retrievability results are proved for ‘behaviour structures’.

In this paper we investigate the effect of assuming determinism on the lattice of equivalences in between sequence/trace equivalence and event structure isomorphism. In the course of the discussion we will sketch parts of this lattice: we will define a number of equivalences and establish their mutual relationships. Hence our paper can be viewed as a contribution to the research area of comparative concurrency semantics. Related work on this topic has been done by POMELLO [21], VAN GLABBEEK & VAANDRAGER [11] and ACETO, DE NICOLA & FANTECHI [1].

§2 EVENT STRUCTURES

2.1. DEFINITION. A (labelled) event structure (over an alphabet A) is a 4-tuple $(E, \leq, \#, l)$, where

- E is a set of events;
- $\leq \subseteq E \times E$ is a partial order satisfying the principle of finite causes:

$$\{e' \in E \mid e' \leq e\} \text{ is finite for } e \in E;$$
- $\# \subseteq E \times E$ is an irreflexive, symmetric relation (the conflict relation) satisfying the principle of conflict heredity:

$$e_1 \# e_2 \leq e_3 \Rightarrow e_1 \# e_3;$$
- $l: E \rightarrow A$ is a labelling function.

As usual we write $e' < e$ for $e' \leq e \wedge e' \neq e$, \geq for \leq^{-1} , and $>$ for $<^{-1}$. We use \sim to denote the relation $E \times E - (\leq \cup \geq \cup \#)$. \sim is called the concurrency relation. By definition $<, =, >, \#$ and \sim form a partition of $E \times E$.

2.2. Note. The components of an event structure \mathbb{E} will be denoted by respectively $E_{\mathbb{E}}, \leq_{\mathbb{E}}, \#_{\mathbb{E}}$ and $l_{\mathbb{E}}$. The derived relations will be denoted $\sim_{\mathbb{E}}, <_{\mathbb{E}}, >_{\mathbb{E}}, \geq_{\mathbb{E}}$. For $e \in E_{\mathbb{E}}$, $pre_{\mathbb{E}}(e)$ denotes the set of events which precede e in the ordering (so $pre_{\mathbb{E}}(e) = \{e' \in E_{\mathbb{E}} \mid e' \leq_{\mathbb{E}} e\}$).

2.3. Graphical representation. In the graphical representation we either depict the events or their labels, depending on what we want to illustrate. The partial order relation is indicated by arrows. The conflict relation is denoted by means of dotted lines. If we draw no relation between events they are concurrent, unless, by means of the transitive and reflexive closure of the arrows, it can be deduced that they are ordered, or, by means of the principle of conflict heredity, it can be deduced that they are in conflict.

2.4. Example. Let the event structure \mathbb{E} be given by:

$$\begin{aligned} E_{\mathbb{E}} &= \{e_1, e_2, e_3, e_4, e_5\} \\ \leq_{\mathbb{E}} &= \{(e_1, e_2), (e_1, e_3), (e_2, e_3)\} \cup \{(e, e) \mid e \in E_{\mathbb{E}}\} \\ \#_{\mathbb{E}} &= \{(x, e_4), (e_4, x) \mid x \in \{e_1, e_2, e_3\}\} \\ l_{\mathbb{E}}(e_i) &= a_i \end{aligned}$$

Graphically we can depict \mathbb{E} as follows:

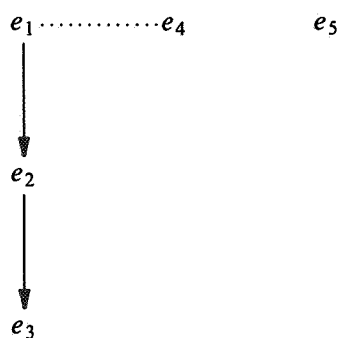


FIGURE 1.

2.5. *Operational meaning of event structures.* The events in an event structure can be anything varying from a clock pulse in a computer, the printing of a file, my act of writing this article, your act of reading it, the next crash of Wall Street, etc.

The partial order relation expresses that some events are causally related to other events or that for all observers the occurrence of certain events will be seen to precede the occurrence of others. For instance, my act of writing this article will precede your act of reading it. On the other hand, your act of reading this article will probably not be causally related to the next crash of Wall Street. The question what, in general, constitutes a causal link, is a metaphysical one and difficult to answer. However, in a lot of practical situations it is perfectly clear what we mean with causality and reasoning about the behaviour of concurrent systems in terms of causality is useful.

The principle of finite causes says that the systems we consider are discrete and that moreover we do not consider situation like



FIGURE 2.

or

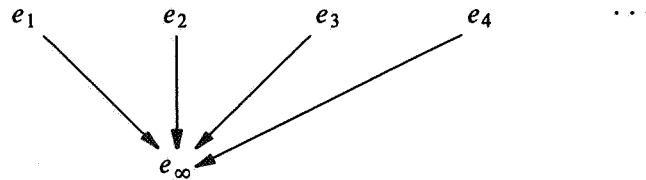


FIGURE 3.

In the first situation it is not clear that any of the e_i can ever happen, in the second situation e_∞ can occur if execution of *all* events e_1, e_2, \dots finishes after a finite amount of time. Because we do not make any assumptions about the time it takes to perform an event, it is possible that e_1 takes 1 second, e_2 takes 2 seconds, etc. In that case e_∞ will never take place.

If two events are in conflict, then at most one of them can occur. As a consequence of the principle of conflict heredity we have that when an event occurs, all its 'causes' must have occurred before. So if two events e and e' are related in the ordering, say $e < e'$, then occurrence of e is a prerequisite for the occurrence of e' . In general it is not the case that after occurrence of e the occurrence of e' is inevitable. It would be possible to allow event structures where one event has two causes, which are in conflict:

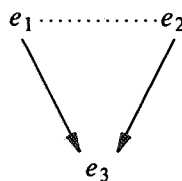


FIGURE 4.

Two interpretations of the above event structure are possible: either one can say that e_3 will never occur because it is impossible that all its causes occur (in that case one can just as well leave e_3 out of the event structure and adopt the principle of conflict heredity), or one can say that e_3 can occur if a

maximal, conflict-free subset of its causes has occurred, so $\{e_1\}$ or $\{e_2\}$.

There are no fundamental reasons to adopt the principles of finite causes and conflict heredity. We have included them in our definition of event structures because this makes an elegant formulation possible of the main result of this paper.

The operational intuitions that we presented in the discussion above, will be defined formally below.

2.6. DEFINITION. Let \mathbf{E} be an event structure and let X be a subset of $E_{\mathbf{E}}$. We say that X is *left-closed* if

$$e \in X \wedge e' \leq_{\mathbf{E}} e \Rightarrow e' \in X.$$

X is *conflict-free* if X does not contain a pair of events which are in conflict, so if $\#_{\mathbf{E}} \cap (X \times X) = \emptyset$. \mathbf{E} is *conflict-free* if $\#_{\mathbf{E}} = \emptyset$. A *configuration* of \mathbf{E} is a finite,¹ left-closed, conflict-free subset of $E_{\mathbf{E}}$. With $\mathcal{C}(\mathbf{E})$ we denote the set of configurations of \mathbf{E} .

2.7. Example. In figure 5 below we have depicted all configurations of the event structure of example 2.4. An arrow is drawn between two configurations if one can be obtained from the other by adding a single event.

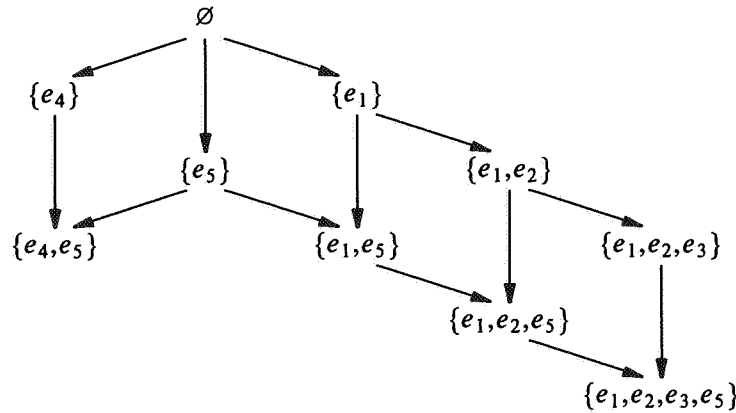


FIGURE 5.

2.8. DEFINITION. For any alphabet Σ , we use Σ^* to denote the set of finite sequences over alphabet Σ and Σ^+ to denote the set of finite nonempty sequences over this alphabet. We write λ for the empty sequence and a for the sequence consisting of the single symbol $a \in \Sigma$. By $\sigma * \sigma'$, sometimes abbreviated $\sigma\sigma'$, we denote the concatenation of sequences σ and σ' . On sequences we define a partial ordering \leq (the *prefix ordering*) by: $\sigma \leq \rho$ iff, for some sequence σ' , $\sigma\sigma' = \rho$. If $\sigma \leq \rho$ we say that σ is a *prefix* of ρ .

2.9. DEFINITION. Let \mathbf{E} be an event structure and let X and Y be configurations of \mathbf{E} .

- i) Let $a \in A$. We say that there is an *a-transition* from X to Y , notation $X \xrightarrow{a}_{\mathbf{E}} Y$, if $Y = X \cup \{e\}$ for some event $e \notin X$ with $I_{\mathbf{E}}(e) = a$.
- ii) An action $a \in A$ is *enabled* in X , notation $X \xrightarrow{a}_{\mathbf{E}}$, if $X \xrightarrow{a}_{\mathbf{E}} X'$ for some configuration X' .
- iii) A sequence of actions $\sigma = a_1 * \dots * a_n \in A^*$ is *enabled* in X , notation $X \xrightarrow{\sigma}_{\mathbf{E}}$, if there exist

1. WINSKEL [28] does not require that configurations are finite.

configurations X_0, \dots, X_n such that $X = X_0$ and for $1 \leq i \leq n$: $X_{i-1} \xrightarrow{a_i} \mathbf{E} X_i$. We say that X_n is obtained from X by the occurrence of σ , notation $X \xrightarrow{\sigma} \mathbf{E} X_n$. We also say that σ is an (action) sequence of X .

- iv) A sequence of events $\alpha = e_1 * \dots * e_n \in E_{\mathbf{E}}^*$ is *enabled* in X , notation $X \xrightarrow{\alpha} \mathbf{E}$, if there exist configurations X_0, \dots, X_n such that $X = X_0$ and for $1 \leq i \leq n$: $e_i \notin X_{i-1}$ and $X_i = X_{i-1} \cup \{e_i\}$. We say that α is an (event) sequence of X .
- v) With $seq_{\mathbf{E}}(X)$ we denote the set of action sequences of X , so $seq_{\mathbf{E}}(X) = \{\sigma \in A^* \mid X \xrightarrow{\sigma} \mathbf{E}\}$.

2.10. PROPOSITION (no junk). Let \mathbf{E} be an event structure and let $e \in E_{\mathbf{E}}$. Then there exists a configuration X of \mathbf{E} with $e \in X$.

PROOF: Take $X = pre_{\mathbf{E}}(e)$. Due to the principle of finite causes X is finite. From the fact that $\leq_{\mathbf{E}}$ is a partial order it follows that X is left-closed. X is conflict-free due to the principle of conflict heredity. Hence X is a configuration. Clearly $e \in X$. \square

§3 THREE BASIC EQUIVALENCES ON EVENT STRUCTURES

We will now define three equivalences on event structures which make increasingly more identifications.

3.1. DEFINITION. An *event structure isomorphism* between two event structures \mathbf{E} and \mathbf{F} is a bijective mapping $f: E_{\mathbf{E}} \rightarrow E_{\mathbf{F}}$ such that:

- $f(e) \leq_{\mathbf{F}} f(e') \Leftrightarrow e \leq_{\mathbf{E}} e'$,
- $f(e) \#_{\mathbf{F}} f(e') \Leftrightarrow e \#_{\mathbf{E}} e'$ and
- $l_{\mathbf{F}}(f(e)) = l_{\mathbf{E}}(e)$.

\mathbf{E} and \mathbf{F} are *isomorphic*, notation $\mathbf{E} \cong \mathbf{F}$, if there exists an event structure isomorphism between them.

3.2. DEFINITION. Let \mathbf{E}, \mathbf{F} be two event structures. A relation $R \subseteq \mathcal{C}(\mathbf{E}) \times \mathcal{C}(\mathbf{F})$ is a *bisimulation* between \mathbf{E} and \mathbf{F} if:

1. $\emptyset R \emptyset$;
2. If XY and $X \xrightarrow{a} \mathbf{E} X'$ for some $a \in A$, then there exists a $Y' \in \mathcal{C}(\mathbf{F})$ such that $Y \xrightarrow{a} \mathbf{F} Y'$ and $X'R Y'$;
3. As 2 but with the roles of X and Y reversed.

\mathbf{E} and \mathbf{F} are *bisimilar*, notation $\mathbf{E} \Leftrightarrow \mathbf{F}$, if there exists a bisimulation between them.

3.3. DEFINITION. Two event structures \mathbf{E} and \mathbf{F} are *sequence equivalent*, notation $\mathbf{E} \equiv_{seq} \mathbf{F}$, if:

$$seq_{\mathbf{E}}(\emptyset) = seq_{\mathbf{F}}(\emptyset).$$

3.3.1. Remark. The semantical notion of sequence equivalence, is usually called *trace* equivalence in the settings of process algebra and trace theory à la REM [23]. However, use of the word trace would be very confusing in a paper on event structures, since event structures are closely related to a completely different type of traces, namely those which are studied in trace theory à la MAZURKIEWICZ [16]. Therefore we have chosen to use the word 'sequence' to denote a finite string of symbols recording the actions in which a process has engaged up to some moment in time. Still we think that the word 'trace' is more suitable for denoting a string of symbols than for denoting an equivalence class of strings (as in trace theory à la Mazurkiewicz) because outside computer science the word trace is associated with 'a mark or line left by something that has passed' ([20]).

3.4. PROPOSITION. \cong , \Leftrightarrow and \equiv_{seq} are equivalence relations and their relations are as indicated below:

$$\cong \Rightarrow \Leftrightarrow \Rightarrow \equiv_{seq}.$$

PROOF: Standard. \square

3.5. Examples. The event structures in figure 6 show that \cong , \Leftrightarrow and \equiv_{seq} are really different equivalences. In the graphical representations we have depicted the labels of the events and not the events themselves.

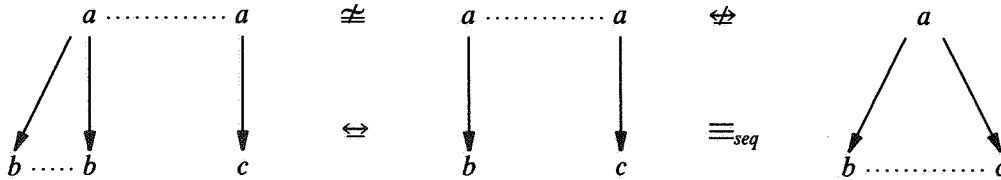


FIGURE 6.

The following definition is central in this paper:

3.6. DEFINITION. Let \mathbb{E} be an event structure. \mathbb{E} is *deterministic* if for all configurations $X \in \mathcal{C}(\mathbb{E})$ we have that whenever $X \xrightarrow{a}_{\mathbb{E}} Y$ and $X \xrightarrow{a}_{\mathbb{E}} Y'$ for some $a \in A$ and $Y, Y' \in \mathcal{C}(\mathbb{E})$, we have that $Y = Y'$.

So an event structure is deterministic if it does not have a configuration with the property that two different events are enabled which have the same label.

3.7. DEFINITION. Let \mathbb{E} be an event structure. Two events $e, e' \in E_{\mathbb{E}}$ are in *immediate conflict*, notation $e \#_{\mathbb{E}} e'$, if they are in conflict and furthermore:

$$e \geq_{\mathbb{E}} f \#_{\mathbb{E}} e' \Rightarrow e = f \quad \text{and} \quad e \#_{\mathbb{E}} f \leq_{\mathbb{E}} e' \Rightarrow f = e'.$$

Using the notion of immediate conflict we can give a 'less operational' characterization of deterministic event structures.

3.8. PROPOSITION. Let \mathbb{E} be an event structure. Then \mathbb{E} is deterministic iff:

$$e \not\leq_{\mathbb{E}} e' \text{ or } e \#_{\mathbb{E}} e' \Rightarrow l_{\mathbb{E}}(e) \neq l_{\mathbb{E}}(e').$$

PROOF: Easy. \square

It is well-known that the linear time - branching time spectrum collapses for deterministic event structures.

3.9. PROPOSITION. Let \mathbb{E}, \mathbb{F} be deterministic event structures. Then: $\mathbb{E} \Leftrightarrow \mathbb{F} \Leftrightarrow \mathbb{E} \equiv_{seq} \mathbb{F}$.

PROOF: ' \Rightarrow ' follows from proposition 3.4. In order to prove ' \Leftarrow ' define a relation $R \subseteq \mathcal{C}(\mathbb{E}) \times \mathcal{C}(\mathbb{F})$ by:

$$X R Y \Leftrightarrow seq_{\mathbb{E}}(X) = seq_{\mathbb{F}}(Y).$$

It is easy to show that R gives a bisimulation between \mathbb{E} and \mathbb{F} . \square

3.10. *Remark.* In a dictionary ([20]) we found the following entry for the word ‘determinism’:

1. a doctrine that all phenomena are determined by preceding occurrences; *esp.* the doctrine that all human acts, choices etc are causally determined and that free will is illusory;
2. a belief in predestination.

One may think that the notion of determinism introduced in definition 3.6 is in conflict with the above description. If one for instance considers the deterministic event structure containing two events labelled a and b which are in conflict, then one may argue that the choice between a and b is not causally determined, that the event structure ‘has a free will’ and ‘may choose’ whether to perform a or b . Therefore one may propose another definition of determinism for event structures which says that an event structure is deterministic iff it is conflict-free. In fact this definition occurs in ACETO, DE NICOLA & FANTECHI [1].

We however prefer our own definition because we like to view event structures as ‘reactive systems’. An event structure model of a concurrent system describes how the system reacts to stimuli received from its environment. In the above example of the event structure with actions a and b , it is completely determined how a system modelled by this event structure will react to external stimuli: the system has no choice.

Now consider the following event structure:

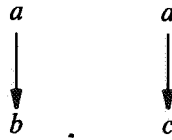


FIGURE 7.

This event structure is conflict-free and hence deterministic in the sense of [1]. However, if the environment offers an a , then there is a choice between the ‘left’ a and the ‘right’ a . Depending on how this choice is resolved by the system, it can engage in b or in c afterwards. Hence one can argue that the event structure exhibits nondeterministic behaviour.

§4 NON-INTERLEAVED EQUIVALENCES

Many people think that bisimulation equivalence, and consequently also sequence equivalence, make too many identifications on event structures to be of use in general. In bisimulation semantics concurrency is not preserved, i.e. for each event structure we can give a bisimilar event structure with an empty concurrency relation. We elaborate on this below.

4.1.1. *DEFINITION.* The *sequentialisation* of an event structure \mathbf{E} , notation $\mathfrak{S}(\mathbf{E})$, is the event structure \mathbf{F} defined by:

- $E_{\mathbf{F}} = \{\alpha \in (E_{\mathbf{E}})^+ \mid \emptyset \xrightarrow{\alpha} \mathbf{E}\}$;
- $\alpha \leq_{\mathbf{F}} \beta$ iff α is a prefix of β ;
- $\#_{\mathbf{F}} = (E_{\mathbf{F}} \times E_{\mathbf{F}}) - (\leq_{\mathbf{F}} \cup \geq_{\mathbf{F}})$;
- $l_{\mathbf{F}}(\alpha * e) = l_{\mathbf{E}}(e)$.

4.1.2. *PROPOSITION.* Let \mathbf{E} be an event structure. Then:

- i) the concurrency relation of $\mathfrak{S}(\mathbf{E})$ is empty,
- ii) $\mathbf{E} \Leftrightarrow \mathfrak{S}(\mathbf{E})$,
- iii) $\mathfrak{S}(\mathbf{E}) \cong \mathfrak{S}(\mathfrak{S}(\mathbf{E}))$.

PROOF: Easy. □

4.2. *Step semantics.* Intuitively, one of the reasons why an event structure is in general different from its sequentialisation is that it sometimes has the possibility to do a number of events simultaneously in one ‘step’. The notion of a ‘step’ immediately suggests refinements of sequence equivalence and bisimulation equivalence which do not disregard concurrency. These refinements will be called step sequence equivalence and step bisimulation equivalence respectively. Step sequences were defined already in [10]. Step bisimulations appear in [19]. In [11] they are called ‘concurrent bisimulations’. Below we give the formal definitions of step sequence equivalence.

4.2.1. DEFINITION. Let \mathbb{E} be an event structure and let X and Y be configurations of \mathbb{E} .

- (i) Let U be a finite subset of $E_{\mathbb{E}}$. We say that Y U -follows X , notation $X[U > Y$, if $X \cap U = \emptyset$, the elements of U are pairwise concurrent (so $\forall e, e' \in U: e \neq e' \Rightarrow e \not\sim_{\mathbb{E}} e'$) and $Y = X \cup U$.
- (ii) Let $U \subseteq E_{\mathbb{E}}$. We say that U is *enabled* in X (U is a *step* from X), notation $X[U >_{\mathbb{E}}$, if $X[U >_{\mathbb{E}} X'$ for some configuration X' of \mathbb{E} .
- (iii) A sequence $\alpha = U_1 * \dots * U_n \in (\text{Pow}(E_{\mathbb{E}}))^*$ is *enabled* in X , notation $X[\alpha >_{\mathbb{E}}$, if there exist configurations X_0, \dots, X_n such that $X = X_0$ and for $1 \leq i \leq n$: $X_{i-1}[U_i >_{\mathbb{E}} X_i$. We say that X_n is *obtained from X by the occurrence of α* , notation $X[\alpha >_{\mathbb{E}} X_n$. We also say that α is an *(event) step sequence of X* .
- (iv) Let $\alpha = U_1 * \dots * U_n \in (\text{Pow}(E_{\mathbb{E}}))^*$ such that $X[\alpha >_{\mathbb{E}} Y$. Let σ be the sequence $l_{\mathbb{E}}(U_1) * \dots * l_{\mathbb{E}}(U_n)$ where $l_{\mathbb{E}}(U_i)$ denotes the multiset of labels of events in U_i . We say that σ is *enabled* in X , notation $X[\sigma >_{\mathbb{E}}$. We also say that σ is an *(action) step sequence of X* , and that Y is *obtained from X by the occurrence of σ* , notation $X[\sigma >_{\mathbb{E}} Y$.
- (v) With $\text{step}_{\mathbb{E}}(X)$ we denote the set of action step sequences of X , so $\text{step}_{\mathbb{E}}(X) = \{\sigma \in (\text{Mul}(A))^* \mid X[\sigma >_{\mathbb{E}}\}$.

4.2.2. DEFINITION. Two event structures \mathbb{E} and \mathbb{F} are *step sequence equivalent*, notation $\mathbb{E} \equiv_{\text{step}} \mathbb{F}$, if:

$$\text{step}_{\mathbb{E}}(\emptyset) = \text{step}_{\mathbb{F}}(\emptyset).$$

4.2.3. PROPOSITION. \equiv_{step} is an equivalence relation. The following relations hold between the equivalences presented thus far:

$$\begin{array}{ccc} \cong & \Rightarrow & \Leftrightarrow \\ \Downarrow & & \Downarrow \\ \equiv_{\text{step}} & \Rightarrow & \equiv_{\text{seq}} \end{array}$$

PROOF: Easy. □

4.2.4. *Examples.* We give some examples which show that the diagram above gives *all* relations between the equivalences. Our first example shows that step semantics (at least sometimes) takes concurrency as a primitive notion.

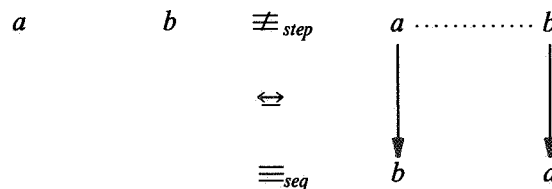


FIGURE 8.

The two leftmost event structures in figure 6 are not isomorphic but they are step sequence equivalent. This follows from the observation that on the domain of event structures with empty concurrency

relation step sequence equivalence and sequence equivalence coincide.

The two rightmost event structures in figure 6 are not bisimilar, but they are step sequence equivalent.

4.3. Partial order semantics. An A -labelled partially ordered set is a triple (X, \leq, l) with X a set, \leq a partial order on X , and $l: X \rightarrow A$ a labelling function. Two such sets (X_0, \leq_0, l_0) and (X_1, \leq_1, l_1) are *isomorphic* if there exists a bijective mapping $f: X_0 \rightarrow X_1$ such that $f(x) \leq_1 f(y) \Leftrightarrow x \leq_0 y$ and $l_1(f(x)) = l_0(x)$. A *partially ordered multiset (pomset)* is an isomorphism class of labelled partially ordered sets. As usual, pomsets can be made setlike by requiring that the events in the partial orders should be chosen from a given set. Below we will view equivalence classes of conflict-free event structures as pomsets.

4.3.1. DEFINITION. The *restriction* of an event structure \mathbf{E} to a set $X \subseteq E_{\mathbf{E}}$ of events is the event structure $\mathbf{E} \upharpoonright X = (X, \leq_{\mathbf{E}} \cap (X \times X), \#_{\mathbf{E}} \cap (X \times X), l_{\mathbf{E}} \upharpoonright X)$.

4.3.2. DEFINITION. Let \mathbf{E} be an event structure and let X be a configuration of \mathbf{E} . The set of *pomsets* of X , notation $\text{pom}_{\mathbf{E}}(X)$, is defined by:

$$\text{pom}_{\mathbf{E}}(X) = \{(\mathbf{E} \upharpoonright (X' - X)) / \cong \mid X \subseteq X' \in \mathcal{C}(\mathbf{E})\}.$$

4.3.3. DEFINITION. Two event structures \mathbf{E} and \mathbf{F} are *pomset equivalent*, notation $\mathbf{E} \equiv_{\text{pom}} \mathbf{F}$, if:

$$\text{pom}_{\mathbf{E}}(\emptyset) = \text{pom}_{\mathbf{F}}(\emptyset).$$

The first systematic study of pomsets is by GRABOWSKI [12], who called them *partial words*. Pomset semantics is advocated by PRATT [22].

4.3.4. PROPOSITION. \equiv_{pom} is an equivalence relation. It fits in our semantical lattice as follows:

$$\begin{array}{ccccc} \cong & & \Rightarrow & & \Leftrightarrow \\ \downarrow & & & & \downarrow \\ \equiv_{\text{pom}} & \Rightarrow & \equiv_{\text{step}} & \Rightarrow & \equiv_{\text{seq}} \end{array}$$

4.3.5. Examples. The two rightmost event structures in figure 6 provide an example of two event structures which are identified in pomset semantics, but distinguished in bisimulation semantics. The remaining examples distinguishing pomset equivalence and the other equivalences are displayed in figure 9 below. The example of figure 10 is interesting because it only contains conflict-free event structures. The example disproves theorem 3.5 of ACETO, DE NICOLA & FANTECHI [1].

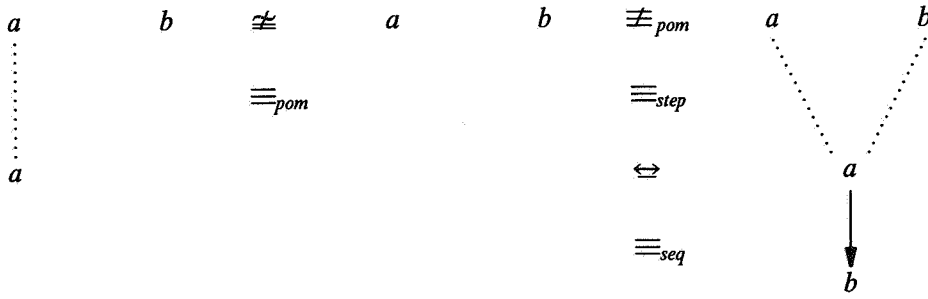


FIGURE 9.

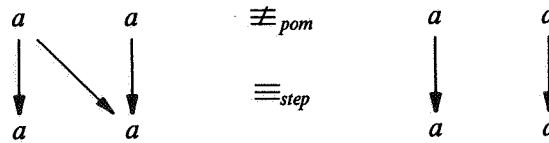


FIGURE 10.

Notice that all these examples contain non-deterministic event structures.

§5 DETERMINISM \rightarrow (EVENT STRUCTURE ISOMORPHISM = STEP SEQUENCE EQUIVALENCE)

Proposition 3.9 stated that bisimulation equivalence and sequence equivalence coincide on the domain of deterministic event structures. Surprisingly, most of the non-interleaved semantics which have been proposed in the literature, also coincide on this domain.

In the introduction of this paper we mentioned a large number of equivalences which are situated in between event structure isomorphism and step sequence equivalence. As a consequence of the following result *all* these equivalences (except for occurrence net equivalence) coincide with event structure isomorphism on the domain of deterministic event structures.

5.1. THEOREM. *Let \mathbb{E}, \mathbb{F} be deterministic event structures. Then: $\mathbb{E} \cong \mathbb{F} \Leftrightarrow \mathbb{E} \equiv_{step} \mathbb{F}$.*

5.2. LEMMA. *Let \mathbb{E} be a deterministic event structure and let X, Y be configurations of \mathbb{E} such that $\mathbb{E} \upharpoonright X \cong \mathbb{E} \upharpoonright Y$. Then: $X = Y$.*

PROOF: Induction on the size of X . If X is the empty set, then Y must be empty too and we are done. Suppose X is nonempty. Let e be a maximal element of X and let $X' = X - \{e\}$. Now we use that there exists an event structure isomorphism f between $\mathbb{E} \upharpoonright X$ and $\mathbb{E} \upharpoonright Y$: we have $\mathbb{E} \upharpoonright X' \cong \mathbb{E} \upharpoonright Y'$ for $Y' = Y - \{f(e)\}$ and furthermore X' and Y' are configurations. Applying the induction hypothesis gives $X' = Y'$. Let $a = l_{\mathbb{E}}(e) = l_{\mathbb{E}}(f(e))$. We have that $X' \xrightarrow{a}_{\mathbb{E}} X$ but also $X' \xrightarrow{a}_{\mathbb{E}} Y$. Now use that \mathbb{E} is deterministic to obtain that $X = Y$. \square

5.3. LEMMA. *Let \mathbb{E} and \mathbb{F} be deterministic event structures. Then: $\mathbb{E} \equiv_{pom} \mathbb{F} \Leftrightarrow \mathbb{E} \cong \mathbb{F}$.*

PROOF: ' \Leftarrow ' is trivial, so the interesting direction is ' \Rightarrow '. Define relation $\sim \subseteq E_{\mathbb{E}} \times E_{\mathbb{F}}$ by:

$$e_0 \sim e_1 \Leftrightarrow_{def} \mathbb{E} \upharpoonright pre_{\mathbb{E}}(e_0) \cong \mathbb{F} \upharpoonright pre_{\mathbb{F}}(e_1).$$

We claim that \sim gives a bijective mapping between $E_{\mathbb{E}}$ and $E_{\mathbb{F}}$. Because $\mathbb{E} \equiv_{pom} \mathbb{F}$, it is obvious that $dom(\sim) = E_{\mathbb{E}}$ and $range(\sim) = E_{\mathbb{F}}$. Suppose that $e_0 \sim e_1$ and $e_0 \sim e_1'$. We show that $e_1 = e_1'$. By definition we have $\mathbb{E} \upharpoonright pre_{\mathbb{E}}(e_0) \cong \mathbb{F} \upharpoonright pre_{\mathbb{F}}(e_1) \cong \mathbb{F} \upharpoonright pre_{\mathbb{F}}(e_1')$. Application of the previous lemma

gives $pre_{\mathbb{F}}(e_1) = pre_{\mathbb{F}}(e_1')$. Since both sets have a unique maximal element, these maximal elements must be identical: $e_1 = e_1'$. In the same way we can prove that if $e_0 \sim e_1$ and $e_0' \sim e_1$, this implies $e_0 = e_0'$. Hence \sim gives a bijection between $E_{\mathbb{E}}$ and $E_{\mathbb{F}}$. It is not hard to see that this bijection is in fact an event structure isomorphism. \square

Proof of theorem 5.1. From the previous results it follows that in order to prove theorem 5.1 it is enough to show that for deterministic event structures \mathbb{E}, \mathbb{F} :

$$\mathbb{E} \equiv_{step} \mathbb{F} \Rightarrow \mathbb{E} \equiv_{pom} \mathbb{F}.$$

By definition this is equivalent to:

$$step_{\mathbb{E}}(\emptyset) = step_{\mathbb{F}}(\emptyset) \Rightarrow pom_{\mathbb{E}}(\emptyset) = pom_{\mathbb{F}}(\emptyset).$$

We will prove a slightly stronger statement, namely:

$$\forall X \in \mathcal{C}(\mathbb{E}) \forall Y \in \mathcal{C}(\mathbb{F}) : step_{\mathbb{E}}(X) = step_{\mathbb{F}}(Y) \Rightarrow pom_{\mathbb{E}}(X) = pom_{\mathbb{F}}(Y).$$

Let $X \in \mathcal{C}(\mathbb{E})$, $Y \in \mathcal{C}(\mathbb{F})$ with $step_{\mathbb{E}}(X) = step_{\mathbb{F}}(Y)$. Let X' be a configuration of \mathbb{E} with $X \subseteq X'$. Let $\alpha_0 = \{e_1\} * \{e_2\} * \dots * \{e_n\}$ be a sequence of singleton steps such that $X[\alpha_0] >_{\mathbb{E}} X'$ and $X' - X = \{e_1, \dots, e_n\}$. Let $\alpha_1 = \{e_1'\} * \{e_2'\} * \dots * \{e_n'\}$ be a step sequence such that $Y[\alpha_1] >_{\mathbb{F}}$ and $l_{\mathbb{E}}(e_i) = l_{\mathbb{F}}(e_i')$ for $1 \leq i \leq n$ (due to the fact that X and Y have the same step sequences, such a sequence will always exist). Let $Y' = Y \cup \{e_1', \dots, e_n'\}$. We claim that the function which maps e_i to e_i' is an event structure isomorphism between $\mathbb{E} \upharpoonright (X' - X)$ and $\mathbb{F} \upharpoonright (Y' - Y)$. For reasons of symmetry we have proved the theorem if we have shown this.

The proof goes by induction to n . The case with $n=0$ is trivial. Now suppose $n > 0$. Due to the fact that X and Y have the same step sequences and due to the determinism of \mathbb{E} and \mathbb{F} , we have:

$$step_{\mathbb{E}}(X \cup \{e_1\}) = step_{\mathbb{F}}(Y \cup \{e_1'\}).$$

Since

$$\begin{aligned} X \cup \{e_1\} [\{e_2\} * \dots * \{e_n\}] >_{\mathbb{E}} X' \quad \text{and} \\ Y \cup \{e_1'\} [\{e_2'\} * \dots * \{e_n'\}] >_{\mathbb{F}} Y', \end{aligned}$$

we can now apply the induction hypothesis which gives:

$$\mathbb{E} \upharpoonright (X' - (X \cup \{e_1\})) \cong \mathbb{F} \upharpoonright (Y' - (Y \cup \{e_1'\})).$$

In order to prove the induction step it is enough to show that for $2 \leq i \leq n$: $e_1 <_{\mathbb{E}} e_i \Leftrightarrow e_1' <_{\mathbb{F}} e_i'$. If $n=1$ we are done, so assume $n \geq 2$. Let for some i , e_i be minimal in $\{e_2, \dots, e_n\}$. Then e_i' is minimal in $\{e_2', \dots, e_n'\}$. We claim that $e_1 <_{\mathbb{E}} e_i \Leftrightarrow e_1' <_{\mathbb{F}} e_i'$. Suppose $e_1 <_{\mathbb{E}} e_i$ but not $e_1' <_{\mathbb{F}} e_i'$. If we show that this leads to a contradiction we have proved the claim because the remaining case is symmetric. If it is not the case that $e_1' <_{\mathbb{F}} e_i'$ then $e_1' \sim_{\mathbb{F}} e_i'$. Due to the minimality of e_i' we have that $Y[\{e_1', e_i'\}] >_{\mathbb{F}}$. Now we use that X and Y have the same step sequences and the fact that \mathbb{E} is deterministic. There must be some f such that $X[\{e_1, f\}] >_{\mathbb{E}}$ and $l_{\mathbb{E}}(f) = l_{\mathbb{F}}(e_i') = l_{\mathbb{E}}(e_i)$. Because $e_1 <_{\mathbb{E}} e_i$, $f \neq e_i$. But now there is a contradiction since we can go from configuration $X \cup \{e_1\}$ with an $l_{\mathbb{E}}(f)$ -transition to $X \cup \{e_1, f\}$ as well as $X \cup \{e_1, e_i\}$.

Now we have proved that for e_i , which are minimal in $\{e_2, \dots, e_n\}$, $e_1 <_{\mathbb{E}} e_i \Leftrightarrow e_1' <_{\mathbb{F}} e_i'$. In order to prove this fact also for e_i which are not minimal, we distinguish between two cases.

1. For all e_i which are minimal in $\{e_2, \dots, e_n\}$, we have that $e_1 <_{\mathbb{E}} e_i$. This implies that $e_1 <_{\mathbb{E}} e_l$ for $2 \leq l \leq n$. Further we have that for all e_i' which are minimal in $\{e_2', \dots, e_n'\}$, $e_1' <_{\mathbb{F}} e_i'$. Consequently $e_1' <_{\mathbb{F}} e_l'$ for $2 \leq l \leq n$, and we are done.
2. There is an e_i which is minimal in $\{e_2, \dots, e_n\}$ such that $e_1 \sim_{\mathbb{E}} e_i$. This means that $e_1' \sim_{\mathbb{F}} e_i'$. We now have the following situation:

$$X \cup \{e_i\} [\{e_1\} * \dots * \{e_{i-1}\} * \{e_{i+1}\} * \dots * \{e_n\}] >_{\mathbb{E}} X' \quad \text{and}$$

$$YU\{e_i'\} \{ \{e_1'\}^* \cdots \{e_{i-1}'\}^* \{e_{i+1}'\}^* \cdots \{e_n'\}^* \} >_{\mathbf{F}} Y'$$

Of course $XU\{e_i\}$ and $YU\{e_i'\}$ have the same step sequences. Application of the induction hypothesis gives:

$$\mathbf{E} \uparrow \{e_1, e_2, \dots, e_{i-1}, e_{i+1}, \dots, e_n\} \cong \mathbf{F} \uparrow \{e_1', e_2', \dots, e_i', e_{i+1}', \dots, e_n'\}.$$

Consequently $e_1 <_{\mathbf{E}} e_l \Leftrightarrow e_1' <_{\mathbf{F}} e_l'$ for $2 \leq l \leq n$. \square

Observe that in the proof of theorem 5.1 we only use that \mathbf{E} and \mathbf{F} have the same sequences of steps containing at most two events.

5.4. The diagram below presents the relations between the equivalences presented thus far when restricted to the domain of deterministic event structures.

$$\begin{array}{c} \cong \Leftrightarrow \equiv_{pom} \Leftrightarrow \equiv_{step} \\ \downarrow \\ \Leftrightarrow \Leftrightarrow \equiv_{seq} \end{array}$$

The example of figure 8 shows that even for deterministic systems there is a difference between arbitrary interleaving and partial order semantics.

§6 ARBITRARY INTERLEAVING VERSUS 'TRUE' CONCURRENCY

One can consider event structures up to step sequence equivalence as an interleaving semantics if one is willing to view a multiset of actions as an action again. In the process algebra languages MELJE and ACP this idea can be implemented by working for instance with an action structure which is the product of a free commutative monoid and a free commutative group. Under this interpretation one can say that for deterministic systems there is no difference between arbitrary interleaving and 'True' concurrency.

Now one can ask the question to what extent a multiset of more than one action can be considered as something which is observable. In a synchronous system like a systolic architecture there is certainly no problem. After each clock tick one can just stop the system and examine which 'cells' have performed an action. The multiset (or set if the system is deterministic) of actions performed by the separate cells gives the step which is performed by the synchronous system. It is much harder to imagine how a 'step' can be observed in an asynchronous system. The only thing I can come up with is that some observer notices the beginning of one action before another action has been finished. In such a situation the observer can conclude that the two actions occur concurrently.

Below, this way of observing concurrent processes is formally implemented by means of an operator *split* on event structures that splits any event e into events e^+ and e^- , which are ordered. One may think of e^+ as the beginning of e and of e^- as the end of e .

6.1.1. DEFINITION. Let \mathbf{E} be an event structure over some alphabet A . Let $A^+ = \{a^+ \mid a \in A\}$ and $A^- = \{a^- \mid a \in A\}$ be two disjoint copies of A . The event structure $\mathbf{F} = \text{split}(\mathbf{E})$ over alphabet $A^+ \cup A^-$ is given by:

$$\begin{aligned} E_{\mathbf{F}} &= \{e^+, e^- \mid e \in E_{\mathbf{E}}\} \\ <_{\mathbf{F}} &= \{(e^x, f^y) \mid x, y \in \{+, -\} \text{ and } e <_{\mathbf{E}} f\} \cup \{(e^+, e^-) \mid e \in E_{\mathbf{E}}\} \\ \#_{\mathbf{F}} &= \{(e^x, f^y) \mid x, y \in \{+, -\} \text{ and } e \#_{\mathbf{E}} f\} \\ l_{\mathbf{F}}(e^+) &= (l_{\mathbf{E}}(e))^+ \\ l_{\mathbf{F}}(e^-) &= (l_{\mathbf{E}}(e))^- \end{aligned}$$

simple argument gives that f^{split} is a bijection. Clearly f^{split} preserves labels. Finally we have that if two events in X are ordered their images under f^{split} are also ordered, and if two events in X are concurrent their images under f^{split} are concurrent too. \square

6.3. PROPOSITION. *Let \mathbf{E} and \mathbf{F} be two event structures. Then: $\mathbf{E} \equiv_{pom} \mathbf{F} \Rightarrow \mathbf{E} \equiv_{split} \mathbf{F}$.*

PROOF: $\mathbf{E} \equiv_{pom} \mathbf{F} \Rightarrow split(\mathbf{E}) \equiv_{pom} split(\mathbf{F}) \Rightarrow split(\mathbf{E}) \equiv_{seq} split(\mathbf{F}) \Rightarrow \mathbf{E} \equiv_{split} \mathbf{F}$. \square

6.4. PROPOSITION. *Let \mathbf{E} and \mathbf{F} be two event structures. Then: $\mathbf{E} \equiv_{split} \mathbf{F} \Rightarrow \mathbf{E} \equiv_{step} \mathbf{F}$.*

PROOF: Let \mathbf{E} and \mathbf{F} be two event structures with $\mathbf{E} \equiv_{split} \mathbf{F}$. Let $\sigma = A_1 * \dots * A_m \in (Mul(A))^*$ with $A_i = \{a_{i1}, \dots, a_{in_i}\}$ be an action step sequence of \mathbf{E} . We must show that σ is also an action step sequence of \mathbf{F} . By symmetry we are ready if we have proved this. The following sequence ρ is an action sequence of $split(\mathbf{E})$:

$$\rho = a_{11}^+ * a_{12}^+ * \dots * a_{1n_1}^+ * a_{11}^- * a_{12}^- * \dots * a_{1n_1}^- * a_{21}^+ * \dots * a_{m1}^+ * \dots * a_{mn_m}^+ * a_{m1}^- * \dots * a_{mn_m}^-$$

Since $\mathbf{E} \equiv_{split} \mathbf{F}$, ρ is also an action sequence of $split(\mathbf{F})$. Hence $split(\mathbf{F})$ has some event sequence α with the property that, if we replace the events in α by their labels, we obtain ρ . Let this α be given by:

$$\alpha = e_{11}^+ * e_{12}^+ * \dots * e_{1n_1}^+ * f_{11}^- * f_{12}^- * \dots * f_{1n_1}^- * e_{21}^+ * \dots * e_{m1}^+ * \dots * e_{mn_m}^+ * f_{m1}^- * \dots * f_{mn_m}^-$$

Note that in general e_{ij} may be different from f_{ij} . However, we do have that $\{e_{i1}, \dots, e_{in_i}\}$ equals $\{f_{i1}, \dots, f_{in_i}\}$.

From the fact that α is an event sequence of $split(\mathbf{F})$ it follows that \mathbf{F} has the event step sequence:

$$\{e_{11}, \dots, e_{1n_1}\} * \dots * \{e_{m1}, \dots, e_{mn_m}\}$$

Hence σ is an action step sequence of \mathbf{F} . \square

6.5. As a consequence of propositions 6.3 and 6.4, split sequence equivalence can be located in our semantical lattice as follows:

$$\begin{array}{ccccc} \cong & & \Rightarrow & & \Leftrightarrow \\ \downarrow & & & & \downarrow \\ \equiv_{pom} & \Rightarrow & \equiv_{split} & \Rightarrow & \equiv_{step} & \Rightarrow & \equiv_{seq} \end{array}$$

6.6. Examples. The essential counterexamples are here:

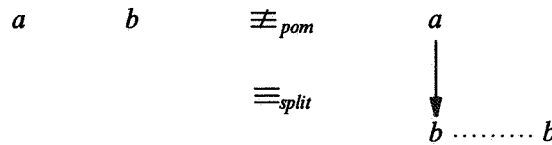


FIGURE 12.

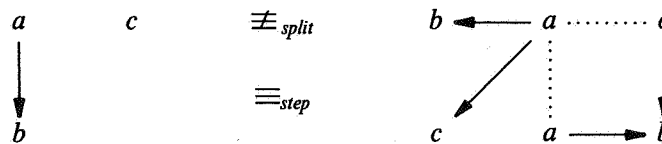


FIGURE 13.

Due to theorem 5.1 and the position of \equiv_{split} in the semantical lattice we have that for deterministic event structures, split bisimulation equivalence and event structure isomorphism coincide. This means that the causal structure of a deterministic concurrent system can be unravelled by observers who are capable to observe the beginning and termination of events.

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REFERENCES

- [1] L. ACETO, R. DE NICOLA & A. FANTECHI (1987): *Testing Equivalences for Event Structures*. In: Proceedings Advanced School on Mathematical Models for the Semantics of Parallelism, 1986 (M. Venturini Zilli, ed.), LNCS 280, Springer-Verlag, pp. 1-20.
- [2] L. ACETO & M. HENNESSY (1988): *Towards Action-Refinement in Process Algebras*. Report 3/88, Computer Science, University of Sussex, Brighton.
- [3] D. AUSTRY & G. BOUDOL (1984): *Algèbre de processus et synchronisations*. Theoretical Computer Science 30(1), pp. 91-131.
- [4] J.A. BERGSTRA & J.W. KLOP (1985): *Algebra of communicating processes with abstraction*. Theoretical Computer Science 37(1), pp. 77-121.
- [5] E. BEST & R. DEVILLERS (1987): *Sequential and Concurrent Behavior in Petri Net Theory*. Theoretical Computer Science 55(1), pp. 87-136.
- [6] G. BOUDOL & I. CASTELLANI (1987): *On the Semantics of Concurrency: Partial Orders and Transition Systems*. In: Proceedings TAPSOFT 87, Vol. I (H. Ehrig, R. Kowalski, G. Levi & U. Montanari, eds.), LNCS 249, Springer-Verlag, pp. 123-137.
- [7] L. CASTELLANO, G. DE MICHELIS & L. POMELLO (1987): *Concurrency vs Interleaving: an instructive example*. Bulletin of the EATCS 31, pp. 12-15.
- [8] P. DEGANO, R. DE NICOLA & U. MONTANARI (1987): *Observational equivalences for concurrency models*. In: Formal Description of Programming Concepts - III, Proceedings of the third IFIP WG 2.2 working conference, Ebberup 1986 (M. Wirsing, ed.), Elsevier Science Publishers B.V. (North Holland), pp. 105-129.
- [9] J. ENGELFRIET (1985): *Determinacy \rightarrow (observation equivalence = trace equivalence)*. Theoretical Computer Science 36(1), pp. 21-25.
- [10] H.J. GENRICH & E. STANKIEWICZ-WIECHNO (1980): *A Dictionary of Some Basic Notions of Petri Nets*. In: Advanced Course on General Net Theory of Processes and Systems, Hamburg 1979 (W. Brauer, ed.), LNCS 84.
- [11] R.J. VAN GLABBEK & F.W. VAANDRAGER (1987): *Petri net models for algebraic theories of concurrency*. In: Proceedings PARLE conference, Eindhoven, Vol. II (Parallel Languages) (J.W. de Bakker, A.J. Nijman & P.C. Treleaven, eds.), LNCS 259, Springer-Verlag, pp. 224-242.
- [12] J. GRABOWSKI (1981): *On Partial Languages*. Fundamenta Informaticae IV(2), pp. 427-498.
- [13] M. HENNESSY (1987): *Axiomatizing Finite Concurrent Processes*. Report 4/87, Computer Science, University of Sussex, Brighton.

- [14] C.A.R. HOARE (1985): *Communicating Sequential Processes*, Prentice-Hall International.
- [15] A. KIEHN (1988): *On the Interrelation between Synchronized and Non-Synchronized Behaviour of Petri Nets*. J. Inf. Process. Cybern. EIK 24(1/2), pp. 3-18.
- [16] A. MAZURKIEWICZ (1987): *Trace theory*. In: Petri Nets: Applications and Relationships to Other Models of Concurrency, Advances in Petri Nets 1986, Part II; Proceedings of an Advanced Course, Bad Honnef, September 1986 (W. Brauer, W. Reisig & G. Rozenberg, eds.), LNCS 255, Springer-Verlag, pp. 279-324.
- [17] R. MILNER (1980): *A Calculus of Communicating Systems*, LNCS 92, Springer-Verlag.
- [18] M. NIELSEN, G.D. PLOTKIN & G. WINSKEL (1981): *Petri nets, event structures and domains, part I*. Theoretical Computer Science 13(1), pp. 85-108.
- [19] M. NIELSEN & P.S. THIAGARAJAN (1984): *Degrees of Non-Determinism and Concurrency: A Petri Net View*. In: Proc. of the 5th Conf. on Found. of Softw. Techn. and Theor. Comp. Sci. (M. Joseph & R. Shyamasundar, eds.), LNCS 181, Springer-Verlag, pp. 89-118.
- [20] PENGUIN (1986): *The New Penguin English Dictionary*, Penguin Books.
- [21] L. POMELLO (1986): *Some equivalence notions for concurrent systems. An overview*. In: Advances in Petri Nets 1985 (G. Rozenberg, ed.), LNCS 222, Springer-Verlag, pp. 381-400.
- [22] V.R. PRATT (1986): *Modelling Concurrency with Partial Orders*. International Journal of Parallel Programming 15(1), pp. 33-71.
- [23] M. REM (1987): *Trace theory and systolic computations*. In: Proceedings PARLE conference, Eindhoven, Vol. I (Parallel Architectures) (J.W. de Bakker, A.J. Nijman & P.C. Treleaven, eds.), LNCS 258, Springer-Verlag, pp. 14-33.
- [24] G. ROZENBERG & P.S. THIAGARAJAN (1986): *Petri nets: basic notions, structure, behaviour*. In: Current Trends in Concurrency (J.W. de Bakker, W.-P. de Roever & G. Rozenberg, eds.), LNCS 224, Springer-Verlag, pp. 585-668.
- [25] M.W. SHIELDS (1982): *Non Sequential Behaviour: 1*. Internal Report CSR-120-82, Department of Computer Science, University of Edinburgh.
- [26] D.A. TAUBNER & W. VOGLER (1987): *The Step Failure Semantics*. In: Proc. STACS 87 (F.J. Brandenburg, G. Vidal-Naquet & M. Wirsing, eds.), LNCS 247, Springer-Verlag, pp. 348-359.
- [27] B.A. TRAKHTENBROT, A. RABINOVICH & J. HIRSHFELD (1988): *Discerning Causality in the Behaviour of Automata*. Technical Report 104/88, Tel Aviv University.
- [28] G. WINSKEL (1987): *Event structures*. In: Petri Nets: Applications and Relationships to Other Models of Concurrency, Advances in Petri Nets 1986, Part II; Proceedings of an Advanced Course, Bad Honnef, September 1986 (W. Brauer, W. Reisig & G. Rozenberg, eds.), LNCS 255, Springer-Verlag, pp. 325-392.

