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Cones and foci for protocol verification revisited

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Cones and Foci for Protocol Verification Revisited

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ABSTRACT

We define a cones and foci proof method, which rephrases the question whether two system specifications are branching bisimilar in terms of proof obligations on relations between data objects. Compared to the original cones and foci method from Groote and Springintveld [GS01], our method is more generally applicable, and does not require a preprocessing step to eliminate τ -loops. We prove soundness of our approach and give an application.

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1. INTRODUCTION

In order to make data a first class citizen in the study of processes, the language μ CRL [GP95] combines the process algebra ACP [BW90] with equational abstract data types [LEW96]. Processes are intertwined with data: Actions and recursion variables are parametrized by data types; an if-then-else construct allows data objects to influence the course of a process; and alternative quantification sums over possibly infinite data domains. Internal activity of a process can be hidden by a hiding operator τ_I , which renames all internal actions (i.e., the actions in the set I) into the hidden action τ [BK85].

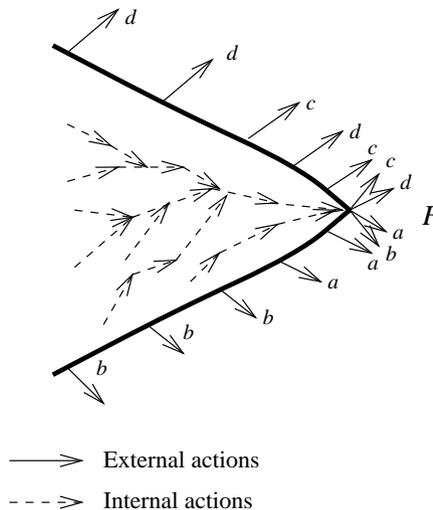
A labeled transition system is associated to each μ CRL specification. Two μ CRL specifications are considered equivalent if the initial states of their labeled transition systems are branching bisimilar [GW96]. Verification of system correctness boils down to checking whether the implementation of a system (with all internal activity hidden) is branching bisimilar to the specification of the desired external behavior of the system. Checking whether two states are branching bisimilar can be performed efficiently [GV90]. The μ CRL toolset [BFG+01] supports the generation of labeled transition systems, together with reduction modulo branching bisimulation equivalence, and allows model checking of temporal logic formulas [CGP00] via a back-end to the CADP toolset [FGK+97].

This approach to verify system correctness has three important drawbacks. First, the labeled transition systems of the μ CRL specifications involved must be generated; often the labeled transition

system of the implementation of a system cannot be generated, as it is too large, or even infinite. Second, this generation usually requires a specific choice for one network or data domain; in other words, only the correctness of an instantiation of the system is proved. Third, support from and rigorous formalization by theorem provers and proof checkers is not readily available.

Groote and Springintveld [GS01] introduced the *cones and foci* method, which rephrases the question whether two μCRL specifications are branching bisimilar in terms of proof obligations on relations between data objects. These proof obligations can be derived by means of algebraic calculations, in general with the help of invariants (i.e., properties of the reachable states) that are proved separately. This method was used in the verification of a considerable number of real-life protocols (e.g., [FGK97, GMP98, SvdZ98]), often with the support of a theorem prover or proof checker.

The main idea of this method is that quite often in the implementation of a system, internal actions progress inertly towards a state in which no internal actions can be executed; such a state is declared to be a *focus point*. The *cone* of a focus point consists of the states that can reach this focus point by a string of internal actions. In the absence of infinite sequences of internal actions, each state belongs to a cone. This core idea is depicted below. Note that the external actions at the edge of the depicted cone can also be executed in the ultimate focus point F ; this is essential for soundness of the cones and foci method, as otherwise internal actions in the cone would not be inert.



Linear process equations [BG94] constitute a restricted class of μCRL specifications in some kind of linear format. Algorithms have been developed to transform μCRL specifications into this linear format [GPU01, GW01, Use02]. In a linear process equation, the states of the associated labeled transition system are data objects.

Assume that the implementation of a system and its desired external behavior are both given in the form of a linear process equation. In the cones and foci method, a *state mapping* ϕ relates each state of the implementation to a state of the desired external behavior. Groote and Springintveld [GS01] formulated *matching criteria*, consisting of relations between data objects, which ensure that states s and $\phi(s)$ are branching bisimilar.

If an implementation, with all internal activity hidden, includes infinite sequences of τ -actions, then Groote and Springintveld [GS01] distinguish between *progressing* and *non-progressing* τ -actions. Their requirements are that (1) there is no infinite sequence of progressing τ -actions, (2) non-progressing τ -actions are only executed at a focus point, and (3) a focus point cannot perform progressing τ -actions. A *pre-abstraction function* divides occurrences of τ -actions in the implementation into progressing and non-progressing ones, and only progressing τ 's are abstracted away; in many cases it is far from trivial to define the proper pre-abstraction. Finally, a special *fair abstraction rule* [BBK87] can be used to try and eliminate the remaining (non-progressing) τ 's.

In this paper, we propose an adaptation of the cones and foci method, in which the cumbersome treatment of infinite sequences of τ -actions is no longer necessary. This improvement of the cones and foci method was conceived during the verification of a sliding window protocol [FGP], where the adaptation simplified matters considerably. As before, the method deals with linear process equations, requires the definition of a state mapping, and generates the same matching criteria. However, we allow the user to freely assign which states are focus points (instead of prescribing that they are the states in which no progressing τ -actions can be performed), as long as each state is in the cone of a focus point. We do allow infinite sequences of internal actions. Since the meaning of recursive specifications that include infinite sequences of τ -actions is ambiguous, we leave the hiding operator τ_I around the μCRL specification of the implementation in place. No distinction between progressing and non-progressing internal actions is needed, and loops of internal actions are eliminated without having to resort to a fair abstraction rule.

We prove that our method is sound modulo branching bisimulation equivalence. Furthermore, we apply our method to the Concurrent Alternating Bit Protocol [KM90], which served as the main example in [GS01]. While the old cones and foci method required a typical cumbersome treatment of τ -loops, here we can take these τ -loops in our stride.

Related Work In compiler correctness, advances have been made to validate programs at a symbolic level with respect to an underlying simulation notion (e.g., [CGP+97, GS99, Nec00]). The methodology surrounding cones and foci incorporates well-known and useful concepts such as the precondition/effect notation [Jon87, LT87], invariants and simulations. Linear process equations resemble the UNITY format [CM88] and recursive applicative program schemes [Cou90]; state mappings are comparable to refinement mappings [LV95, PSS98] and simulation [FP97]. Van der Zwaag [vdZ01] gave an adaptation of the cones and foci method from [GS01] to a timed setting, modulo timed branching bisimulation equivalence. We leave it as an open question whether our innovations for the cones and foci method can also be introduced in this timed setting.

2. PRELIMINARIES

2.1 μCRL

μCRL [GP95] is a language for specifying distributed systems and protocols in an algebraic style. It is based on process algebra extended with equational abstract data types. In a μCRL specification, one part specifies the data types, while a second part specifies the process behavior. We do not describe the treatment of data types in μCRL in detail. For our purpose it is sufficient that processes can be parametrized with data. We assume the data sort of booleans $Bool$ with constant \top and F , and the usual connectives \wedge , \vee , \neg and \Rightarrow . For a boolean b , we abbreviate $b = \top$ to b and $b = \text{F}$ to $\neg b$.

The specification of a process is constructed from action names, recursion variables and process algebraic operators. Actions and recursion variables carry zero or more data parameters. There are two predefined actions in μCRL : δ represents deadlock, and τ a hidden action. These two actions never carry data parameters.

Processes are represented by process terms, which describe the order in which the actions from a set Act may happen. A process term consists of action names and recursion variables combined by process algebraic operators. $p \cdot q$ denotes sequential composition and $p + q$ non-deterministic choice, summation $\sum_{d:D} p(d)$ provides the possibly infinite choice over a data type D , and the conditional construct $p \triangleleft b \triangleright q$ with b a data term of sort $Bool$ behaves as p if b and as q if $\neg b$. Parallel composition $p \parallel q$ interleaves the actions of p and q ; moreover, actions from p and q may also synchronize to a communication action, when this is explicitly allowed by a predefined communication function. Two actions can only synchronize if their data parameters are semantically the same, which means that communication can be used to represent data transfer from one system component to another. Encapsulation $\partial_H(p)$, which renames all occurrences in p of actions from the set H into δ , can be used to force actions into communication. Finally, hiding $\tau_I(p)$ renames all occurrences in p of actions from the set I into τ . The syntax and semantics of μCRL are given in [GP95].

2.2 Labeled transition systems

Labeled transition systems (LTSs) capture the operational behavior of concurrent systems. An LTS consists of transitions $s \xrightarrow{a} s'$, denoting that the state s can evolve into the state s' by the execution of action a . To each μCRL specification belongs an LTS, defined by the structural operational semantics for μCRL in [GP95].

Definition 2.1 (Labeled transition system) A *labeled transition system* is a tuple $(S, Lab, \rightarrow, s_0)$, where S is a set of states, Lab a set of transition labels, $\rightarrow \subseteq S \times Lab \times S$ a transition relation, and s_0 the initial state. A transition (s, ℓ, s') is denoted by $s \xrightarrow{\ell} s'$.

Here, S consists of μCRL specifications, and Lab consists of actions from $Act \cup \{\tau\}$ parametrized by data. We define *branching bisimilarity* [GW96] between states in LTSs. Branching bisimulation is an equivalence relation [Bas96].

Definition 2.2 (Branching bisimulation) Assume an LTS. A *branching bisimulation relation* B is a symmetric binary relation on states such that if $s B t$ and $s \xrightarrow{\ell} s'$, then

- either $\ell = \tau$ and $s' B t$;
- or there is a sequence of (zero or more) τ -transitions $t \xrightarrow{\tau} \dots \xrightarrow{\tau} t_0$ such that $s B t_0$ and $t_0 \xrightarrow{\ell} t'$ with $s' B t'$.

Two states s and t are *branching bisimilar*, denoted by $s \leftrightarrow_b t$, if there is a branching bisimulation relation B such that $s B t$.

The μCRL toolset [BFG+01] supports the generation of labeled transition systems of μCRL specifications, together with reduction modulo branching bisimulation equivalence and model checking of temporal logic formulas. This approach has been used to analyze a wide range of protocols and distributed systems (e.g., [AL01, GPW03, Pan02, PVE02]).

In this paper we focus on analyzing protocols and distributed systems on the level of their symbolic specifications.

2.3 Linear process equations

A *linear process equation* (LPE) is a one-line μCRL specification consisting of actions, summations, sequential compositions and conditional constructs. In particular, an LPE does not contain any parallel operators, encapsulations or hidings. In essence an LPE is a vector of data parameters together with a list of condition, action and effect triples, describing when an action may happen and what is its effect on the vector of data parameters. Each μCRL specification that does not include successful termination can be transformed into an LPE [Use02].¹

Definition 2.3 (Linear process equation) A *linear process equation* is a μCRL specification of the form

$$X(d:D) = \sum_{a \in Act \cup \{\tau\}} \sum_{e:E} a(f_a(d,e)) \cdot X(g_a(d,e)) \triangleleft h_a(d,e) \triangleright \delta$$

where $f_a : D \times E \rightarrow D$, $g_a : D \times E \rightarrow D$ and $h_a : D \times E \rightarrow Bool$ for each $a \in Act \cup \{\tau\}$.

¹To cover μCRL specifications with successful termination, LPEs should also include a summand $\sum_{a \in Act \cup \{\tau\}} \sum_{e:E} a(f_a(d,e)) \triangleleft h_a(d,e) \triangleright \delta$. The cones and foci method extends to this setting without any complication. However, this extension would complicate the matching criteria in Definition 3.3. For the sake of presentation, successful termination is not taken into account here.

The LPE in Definition 2.3 has exactly one LTS as its solution.² In this LTS, the states are data elements $d:D$ (where D may be a Cartesian product of n data types, meaning that d is a tuple (d_1, \dots, d_n)) and the transition labels are actions parametrized with data. The LPE expresses that state d can perform $a(f_a(d, e))$ to end up in state $g_a(d, e)$, under the condition that $h_a(d, e)$ is true. The data type E gives LPEs a more general form, as not only the data parameter $d:D$ but also the data parameter $e:E$ can influence the parameter of action a , the condition h_a and the resulting state g_a .

Definition 2.4 (Invariant) A mapping $\mathcal{I} : D \rightarrow Bool$ is an invariant for an LPE, written as in Definition 2.3, if for all $a \in Act \cup \{\tau\}$, $d:D$ and $e:E$,

$$\mathcal{I}(d) \wedge h_a(d, e) \Rightarrow \mathcal{I}(g_a(d, e)).$$

Intuitively, an invariant characterizes the set of reachable states of an LPE. That is, if $\mathcal{I}(d)$, and if one can involve from state d to state d' in zero or more transitions, then $\mathcal{I}(d')$. Namely, if \mathcal{I} holds in state d and it is possible to execute $a(f_a(d, e))$ in this state (meaning that $h_a(d, e)$), then it is ensured that \mathcal{I} holds in the resulting state $g_a(d, e)$. Invariants tend to play a crucial role in algebraic verifications of system correctness that involve data.

3. CONES AND FOCI

In this section, we present our version of the cones and foci method [GS01]. Suppose that we have an LPE $X(d:D)$ (including internal actions from a set I , which will be hidden) specifying the implementation of a system, and an LPE $Y(d':D')$ (without internal actions) specifying the desired input/output behavior of this system. Furthermore, assume an invariant $\mathcal{I} : D \rightarrow Bool$ characterizing the reachable states of X . We want to prove that the implementation exhibits the desired input/output behavior.

We assume the presence of an invariant $\mathcal{I} : D \rightarrow Bool$ for X . In the cones and foci method, a *state mapping* $\phi : D \rightarrow D'$ relates each state of the implementation X to a state of the desired external behavior Y . Furthermore, some states in D are designated to be *focus points*. In contrast with the approach of [GS01], we allow to freely assign focus points, as long as each state $d:D$ of X with $\mathcal{I}(d)$ can reach a focus point by a sequence of internal transitions. If a number of *matching criteria* for $d:D$ are fulfilled, consisting of relations between data objects, and if $\mathcal{I}(d)$, then the states d and $\phi(d)$ are guaranteed to be branching bisimilar. These matching criteria require that (A) after hiding, all internal transitions of d become invisible, (B) each external transition of d can be mimicked by $\phi(d)$, and (C) if d is a focus point, then vice versa each transition of $\phi(d)$ can be mimicked by d .

We start with defining the predicate FC , designating the focus points of X in D . Next we define the state mapping together with its matching criteria.

Definition 3.1 (Focus point) A *focus condition* is a mapping $FC : D \rightarrow Bool$. If $FC(d)$, then d is called a *focus point*.

Definition 3.2 (State mapping) A *state mapping* is of the form $\phi : D \rightarrow D'$.

Definition 3.3 (Matching criteria) Let the LPE X be of the form

$$X(d:D) = \sum_{a \in Act} \sum_{e:E} a(f_a(d, e)) \cdot X(g_a(d, e)) \triangleleft h_a(d, e) \triangleright \delta.$$

Furthermore, let the LPE Y be of the form

$$Y(d':D') = \sum_{a \in Act \setminus I} \sum_{e:E} a(f'_a(d', e)) \cdot Y(g'_a(d', e)) \triangleleft h'_a(d', e) \triangleright \delta.$$

A state mapping $\phi : D \rightarrow D'$ satisfies the *matching criteria* for $d:D$ if for all $a \in Act \setminus I$ and $c \in I$:

²LPEs exclude “unguarded” recursive specifications such as $X = X$, which have multiple solutions.

- I $\forall e:E (h_c(d, e) \Rightarrow \phi(d) = \phi(g_c(d, e)));$
- II $\forall e:E (h_a(d, e) \Rightarrow h'_a(\phi(d), e));$
- III $FC(d) \Rightarrow \forall e:E (h'_a(\phi(d), e) \Rightarrow h_a(d, e));$
- IV $\forall e:E (h_a(d, e) \Rightarrow f_a(d, e) = f'_a(\phi(d), e));$
- V $\forall e:E (h_a(d, e) \Rightarrow \phi(g_a(d, e)) = g'_a(\phi(d), e)).$

Matching criterion I requires that after hiding, all internal c -transitions from d are invisible, meaning that d and $g_c(d, e)$ are branching bisimilar. Criteria II, IV and V express that each external transition of d can be simulated by $\phi(d)$. Finally, criterion III expresses that if d is a focus point, then each external transition of $\phi(d)$ can be simulated by d .

Theorem 3.4 Assume LPEs $X(d:D)$ and $Y(d':D')$ written as in Definition 3.3. Let $I \subseteq Act$, and let $\mathcal{I} : D \rightarrow Bool$ be an invariant for X . Suppose that for all $d:D$ with $\mathcal{I}(d)$,

- 1. $\phi : D \rightarrow D'$ satisfies the matching criteria for d , and
- 2. there is a $\hat{d}:D$ such that $FC(\hat{d})$ and $d \xrightarrow{c_1} \dots \xrightarrow{c_n} \hat{d}$ with $c_1, \dots, c_n \in I$ in the LTS for X .

Then for all $d:D$ with $\mathcal{I}(d)$,

$$\tau_I(X(d)) \xleftrightarrow{b} Y(\phi(d)).$$

Proof. We assume without loss of generality that D and D' are disjoint. Define $B \subseteq D \cup D' \times D \cup D'$ as the smallest relation such that whenever $\mathcal{I}(d)$ for a $d:D$ then $dB\phi(d)$ and $\phi(d)Bd$. Clearly, B is symmetric. We show that B is a branching bisimulation relation.

Let sBt and $s \xrightarrow{\ell} s'$. First consider that case where $\phi(s) = t$. By definition of B we have $\mathcal{I}(s)$.

- 1. If $\ell = \tau$, then $h_c(s, e)$ and $s' = g_c(s, e)$ for some $c \in I$ and $e:E$. By matching criterion I, $\phi(g_c(s, e)) = t$. Moreover, $\mathcal{I}(s)$ and $h_c(s, e)$ together imply $\mathcal{I}(g_c(s, e))$. Hence, $g_c(s, e)Bt$.
- 2. If $\ell \neq \tau$, then $h_a(s, e)$, $s' = g_a(s, e)$ and $\ell = a(f_a(s, e))$ for some $a \in Act \setminus I$ and $e:E$. By matching criteria II and IV, $h'_a(t, e)$ and $f_a(s, e) = f'_a(t, e)$. Hence, $t \xrightarrow{a(f_a(s, e))} g'_a(t, e)$. Moreover, $\mathcal{I}(s)$ and $h_a(s, e)$ together imply $\mathcal{I}(g_a(s, e))$, and matching criterion V yields $\phi(g_a(s, e)) = g'_a(t, e)$, so $g_a(s, e)Bg'_a(t, e)$.

Next consider the case where $s = \phi(t)$. Since $s \xrightarrow{\ell} s'$, for some $a \in Act \setminus I$ and $e:E$, $h'_a(s, e)$, $s' = g'_a(s, e)$ and $\ell = a(f'_a(s, e))$. By definition of B we have $\mathcal{I}(t)$.

- 1. If $FC(t)$, then by matching criterion III, $h_a(t, e)$. So by matching criterion IV, $f_a(t, e) = f'_a(s, e)$. Hence, $t \xrightarrow{a(f'_a(s, e))} g_a(t, e)$. Moreover, $\mathcal{I}(t)$ and $h_a(t, e)$ together imply $\mathcal{I}(g_a(t, e))$, and matching criterion V yields $\phi(g_a(t, e)) = g'_a(s, e)$, so $g'_a(s, e)Bg_a(t, e)$.
- 2. If $\neg FC(t)$, then there is a $\hat{t}:D$ with $FC(\hat{t})$ such that $t \xrightarrow{c_1} \dots \xrightarrow{c_n} \hat{t}$ with $c_1, \dots, c_n \in I$ in the LTS for X . This implies that $t \xrightarrow{\tau} \dots \xrightarrow{\tau} \hat{t}$ in the LTS for $\tau_I(X)$. Invariant \mathcal{I} , so also the matching criteria, hold for all states on this τ -path. Repeatedly applying matching criterion I we get $\phi(\hat{t}) = \phi(t) = s$. So matching criterion III together with $h'_a(s, e)$ yields $h_a(\hat{t}, e)$. Then by matching criterion IV, $f_a(\hat{t}, e) = f'_a(s, e)$, so $t \xrightarrow{\tau} \dots \xrightarrow{\tau} \hat{t} \xrightarrow{a(f'_a(s, e))} g_a(\hat{t}, e)$. Moreover, $\mathcal{I}(\hat{t})$ and $h_a(\hat{t}, e)$ together imply $\mathcal{I}(g_a(\hat{t}, e))$, and matching criterion V yields $\phi(g_a(\hat{t}, e)) = g'_a(s, e)$, so $sB\hat{t}$ and $g'_a(s, e)Bg_a(\hat{t}, e)$.

Concluding, B is a branching bisimulation. \(\square\)

We note that Groote and Springintveld [GS01] proved for their notion of their cones and foci method that it can be derived from the axioms of μCRL , which implies that their method is sound modulo branching bisimulation equivalence. We leave it as future work to try and derive our cones and foci method from the axioms of μCRL .

4. APPLICATION TO THE CABP

Groote and Springintveld [GS01] proved correctness of the Concurrent Alternating Bit Protocol (CABP) [KM90] as an application of their cones and foci method. Here we redo their correctness proof using our version of the cones and foci method, where in contrast to [GS01] we can take loops of internal activity in our stride.

In the CABP, data elements d_1, d_2, \dots are communicated from a data transmitter S to a data receiver R via a lossy channel, so that a message can be corrupted or lost. Therefore, acknowledgments are sent from R to S again via a lossy channel. In the CABP, sending and receiving of acknowledgments is decoupled from R and S, in the form of separate components AS and AR, respectively, where AS autonomously sends acknowledgments to AR. This ensures a better use of available bandwidth.

S attaches a bit 0 to data elements d_{2k-1} and a bit 1 to data elements d_{2k} , and AS sends back the attached bit to acknowledge reception. S keeps on sending a pair (d_i, b) until AR receives the bit b and sends the message ac to S; then S starts sending the next pair $(d_{i+1}, 1 - b)$. Alternation of the attached bit enables R to determine whether a received datum is really new, and alternation of the acknowledging bit enables AR to determine which datum is being acknowledged.

The CABP contains unbounded internal behavior, which occurs when a channel eternally corrupts or loses the same datum or acknowledgment. The fair abstraction paradigm [BBK87], which underlies branching bisimulation, says that such infinite sequences of faulty behavior do not exist in reality, because the chance of a channel failing infinitely often is zero. Groote and Springintveld [GS01] defined a pre-abstraction function to hide all internal actions except those that are executed in focus points, and used Koomen's fair abstraction rule [BBK87] to eliminate the remaining loops of internal actions. In our adaptation of the cones and foci method, focus points can perform internal actions, so neither pre-abstraction nor Koomen's fair abstraction rule are needed here.

The structure of the CABP is shown in Figure 1. The CABP system is built from six components.

S is a *data transmitter*, which reads data from port 1 and transmits such a datum repeatedly via channel K, until an acknowledgment ac regarding this datum is received from AR.

K is a lossy *data transmission channel*, which transfers data from S to R. Either it delivers the datum correctly, or it can make two sorts of mistakes: lose the datum or change it into the checksum error ce .

R is a *data receiver*, which receives data from K, sends freshly received data into port 2, and sends an acknowledgment to AS via port 5.

AS is an *acknowledgment transmitter*, which receives an acknowledgment from R and repeatedly transmits it via L to AR.

L is a lossy *acknowledgment transmission channel*, which transfers acknowledgments from AS to AR. Either it delivers the acknowledgment correctly, or it can make two sorts of mistakes: lose the acknowledgment or change it into an acknowledgment error ae .

AR is an *acknowledgment receiver*, which receives acknowledgments from L and passes them on to S.

The components can perform read $r_n(\dots)$ and send $s_n(\dots)$ actions to transport data through port n . A read and a send action over the same port n can synchronize into a communication action $c_n(\dots)$.

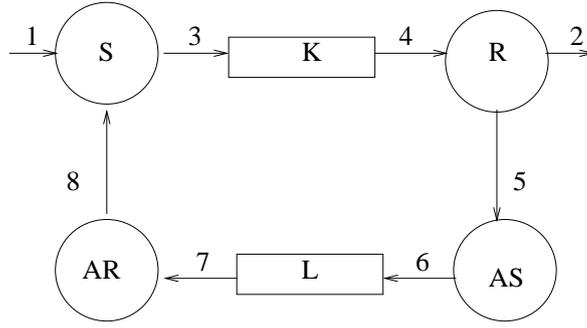


Figure 1: The structure of the CABP

4.1 Implementation and external behavior

We give descriptions of the data types and each component's specification in μCRL , which were originally presented in [GS01]. In each summand of the μCRL specifications below, we take the convenience to only present the parameters whose values are changed.

We use the sort Nat of natural numbers, and the sort Bit with elements b_0 and b_1 with an inversion function $\text{inv} : \text{Bit} \rightarrow \text{Bit}$ to model the alternating bit. The sort D contains the data elements to be transferred. The sort Frame consists of pairs $\langle d, b \rangle$ with $d:D$ and $b:\text{Bit}$. Frame also contains two error messages, ce for checksum error and ae for acknowledgment error. $\text{eq} : S \times S \rightarrow \text{Bool}$ coincides with the equality relation between elements of the sort S .

The data transmitter S reads a datum at port 1 and repeatedly transmits the datum with a bit b_s attached at port 3 until it receives an acknowledgment ac through port 8; after that, the bit-to-be-attached is inverted. The parameter i_s is used to model the state of the data transmitter.

Definition 4.1 (Data transmitter)

$$\begin{aligned} & S(d_s:D, b_s:\text{Bit}, i_s:\text{Nat}) \\ = & \sum_{d:D} r_1(d) \cdot S(d/d_s, 2/i_s) \triangleleft \text{eq}(i_s, 1) \triangleright \delta \\ + & (s_3(\langle d_s, b_s \rangle) \cdot S + r_8(ac) \cdot S(\text{inv}(b_s)/b_s, 1/i_s)) \triangleleft \text{eq}(i_s, 2) \triangleright \delta \end{aligned}$$

The data transmission channel K reads a datum at port 3. It can do one of three things: it deliver the datum correctly via port 4, it lose the datum, or it corrupt the datum by changing it into ce . The non-deterministic choice between the three options is modeled by the action j . b_k is the attached alternating bit for K . And its state is modeled by the parameter i_k .

Definition 4.2 (Data transmission channel)

$$\begin{aligned} & K(d_k:D, b_k:\text{Bit}, i_k:\text{Nat}) \\ = & \sum_{d:D} \sum_{b:\text{Bit}} r_3(\langle d, b \rangle) \cdot K(d/d_k, b/b_k, 2/i_k) \triangleleft \text{eq}(i_k, 1) \triangleright \delta \\ + & (j \cdot K(1/i_k) + j \cdot K(3/i_k) + j \cdot K(4/i_k)) \triangleleft \text{eq}(i_k, 2) \triangleright \delta \\ + & s_4(\langle d_k, b_k \rangle) \cdot K(1/i_k) \triangleleft \text{eq}(i_k, 3) \triangleright \delta \\ + & s_4(ce) \cdot K(1/i_k) \triangleleft \text{eq}(i_k, 4) \triangleright \delta \end{aligned}$$

The data receiver R reads a datum at port 4. If the datum is not a checksum ce and if the bit attached is the expected bit, it sends the received datum into port 2 and sends an acknowledgment ac via port 5, and the bit-to-be-expected is inverted. If the datum is a checksum error or the bit attached is not the expected one, the datum is simply ignored. The parameter i_r is used to model the state of the data receiver.

Definition 4.3 (Data receiver)

$$\begin{aligned}
& R(d_r:D, b_r:Bit, i_r:Nat) \\
= & \sum_{d:D} r_4((d, b_r)) \cdot R(d/d_r, 2/i_r) \triangleleft eq(i_r, 1) \triangleright \delta \\
+ & (r_4(ce) + \sum_{d:D} r_4((d, inv(b_r)))) \cdot R \triangleleft eq(i_r, 1) \triangleright \delta \\
+ & s_2(d_r) \cdot R(3/i_r) \triangleleft eq(i_r, 2) \triangleright \delta \\
+ & s_5(ac) \cdot R(inv(b_r)/b_r, 1/i_r) \triangleleft eq(i_r, 3) \triangleright \delta
\end{aligned}$$

The acknowledgment transmitter AS repeats sending its acknowledgment bit b'_r via port 6, until it receives an acknowledgment ac from port 5, after which the acknowledgment bit is inverted.

Definition 4.4 (Acknowledgment transmitter)

$$\begin{aligned}
& AS(b'_r:Bit) \\
= & r_5(ac) \cdot AS(inv(b'_r)) + s_6(b'_r) \cdot AS
\end{aligned}$$

The acknowledgment transmission channel L reads an acknowledgment bit from port 6. It non-deterministically does one of three things: deliver it correctly via port 7, lose the acknowledgment, or corrupt the acknowledgment by changing it to ae . The non-deterministic choice between the three options is modeled by action j . b_l is the attached alternating bit for L. And its state is modeled by the parameter i_l .

Definition 4.5 (Acknowledgment transmission channel)

$$\begin{aligned}
& L(b_l:Bit, i_l:Nat) \\
= & \sum_{b:Bit} r_6(b) \cdot L(b/b_l, 2/i_l) \triangleleft eq(i_l, 1) \triangleright \delta \\
+ & (j \cdot L(1/i_l) + j \cdot L(3/i_l) + j \cdot L(4/i_l)) \triangleleft eq(i_l, 2) \triangleright \delta \\
+ & s_7(b_l) \cdot L(1/i_l) \triangleleft eq(i_l, 3) \triangleright \delta \\
+ & s_7(ae) \cdot L(1/i_l) \triangleleft eq(i_l, 4) \triangleright \delta
\end{aligned}$$

The acknowledgment receiver AR reads an acknowledgment bit from port 7. If the bit is the expected one, it sends an acknowledgment ac to the data transmitter S via port 8, after which the bit-to-be-expected is inverted. Acknowledgments errors ae or unexpected bits are ignored.

Definition 4.6 (Acknowledgment receiver)

$$\begin{aligned}
& AR(b'_s:Bit, i'_s:Nat) \\
= & r_7(b'_s) \cdot AR(2/i'_s) \triangleleft eq(i'_s, 1) \triangleright \delta \\
+ & (r_7(ae) + r_7(inv(b'_s))) \cdot AR \triangleleft eq(i'_s, 1) \triangleright \delta \\
+ & s_8(ac) \cdot AR(inv(b'_s)/b'_s, 1/i'_s) \triangleleft eq(i'_s, 2) \triangleright \delta
\end{aligned}$$

The μ CRL specification of the CABP is obtained by putting the six components in parallel and encapsulating the internal actions at ports $\{3, 4, 5, 6, 7, 8\}$. Synchronization between the components is modeled by communication actions at connecting ports.

Definition 4.7 Let H denote $\{s_3, r_3, s_4, r_4, s_5, r_5, s_6, r_6, s_7, r_7, s_8, r_8\}$.

$$CABP(d:D) = \partial_H(S(d, b_0, 1) \parallel AR(b_0, 1) \parallel K(d, b_1, 1) \parallel L(b_1, 1) \parallel R(d, b_0, 1) \parallel AS(b_1))$$

Next the CABP is expanded to a LPE *Sys*. We first group S and AR, respectively R and AS, together. This step is to dispose of the parameters b'_s and b'_r .

Definition 4.8 Let $d_s, d_r, d_k : D$, $b_s, b_r, b_k, b_l : \text{Bit}$ and $i_s, i'_s, i_r, i_k, i_l : \text{Nat}$. We define:

$$\begin{aligned} SAR(d_s, b_s, i_s, i'_s) &= S(d_s, b_s, i_s) \parallel AR(b_s, i'_s) \\ RAS(d_r, b_r, i_r) &= R(d_r, b_r, i_r) \parallel AS(\text{inv}(b_r)) \\ Sys(d_s, b_s, i_s, i'_s, d_r, b_r, i_r, d_k, b_k, i_k, b_l, i_l) \\ &= \partial_H(SAR(d_s, b_s, i_s, i'_s) \parallel K(d_k, b_k, i_k) \parallel L(b_l, i_l) \parallel RAS(d_r, b_r, i_r)) \end{aligned}$$

Lemma 4.9 For all $d : D$ we have

$$CABP(d) = Sys(d, b_0, 1, 1, d, b_0, 1, d, b_1, 1, b_1, 1)$$

Proof. This is straightforward by the definitions. \square

Lemma 4.10 In each summand of the LPE for Sys below, we only present the parameters whose values are changed. It holds that

$$\begin{aligned} & Sys(d_s : D, b_s : \text{Bit}, i_s : \text{Nat}, i'_s : \text{Nat}, d_r : D, b_r : \text{Bit}, i_r : \text{Nat}, d_k : D, b_k : \text{Bit}, i_k : \text{Nat}, b_l : \text{Bit}, i_l : \text{Nat}) \\ = & \sum_{d : D} r_1(d) \cdot Sys(d/d_s, 2/i_s) \triangleleft eq(i_s, 1) \triangleright \delta & (1) \\ + & c_3((d_s, b_s)) \cdot Sys(d_s/d_k, b_s/b_k, 2/i_k) \triangleleft eq(i_s, 2) \wedge eq(i_k, 1) \triangleright \delta & (2) \\ + & (j \cdot Sys(1/i_k) + j \cdot Sys(3/i_k) + j \cdot Sys(4/i_k)) \triangleleft eq(i_k, 2) \triangleright \delta & (3) \\ + & c_4((d_k, b_r)) \cdot Sys(d_k/d_r, 2/i_r, 1/i_k) \triangleleft eq(i_r, 1) \wedge eq(b_r, b_k) \wedge eq(i_k, 3) \triangleright \delta & (4) \\ + & c_4((d_k, b_r)) \cdot Sys(1/i_k) \triangleleft eq(i_r, 1) \wedge eq(b_r, \text{inv}(b_k)) \wedge eq(i_k, 3) \triangleright \delta & (5) \\ + & c_4(ce) \cdot Sys(1/i_k) \triangleleft eq(i_r, 1) \wedge eq(i_k, 4) \triangleright \delta & (6) \\ + & s_2(d_r) \cdot Sys(3/i_r) \triangleleft eq(i_r, 2) \triangleright \delta & (7) \\ + & c_5(ac) \cdot Sys(\text{inv}(b_r)/b_r, 1/i_r) \triangleleft eq(i_r, 3) \triangleright \delta & (8) \\ + & c_6(\text{inv}(b_r)) \cdot Sys(\text{inv}(b_r)/b_l, 2/i_l) \triangleleft eq(i_l, 1) \triangleright \delta & (9) \\ + & (j \cdot Sys(1/i_l) + j \cdot Sys(3/i_l) + j \cdot Sys(4/i_l)) \triangleleft eq(i_l, 2) \triangleright \delta & (10) \\ + & c_7(b_l) \cdot Sys(1/i_l, 2/i'_s) \triangleleft eq(i'_s, 1) \wedge eq(b_l, b_s) \wedge eq(i_l, 3) \triangleright \delta & (11) \\ + & c_7(b_l) \cdot Sys(1/i_l) \triangleleft eq(i'_s, 1) \wedge eq(b_l, \text{inv}(b_s)) \wedge eq(i_l, 3) \triangleright \delta & (12) \\ + & c_7(ac) \cdot Sys(1/i_l) \triangleleft eq(i'_s, 1) \wedge eq(i_l, 4) \triangleright \delta & (13) \\ + & c_8(ac) \cdot Sys(\text{inv}(b_s)/b_s, 1/i_s, 1/i'_s) \triangleleft eq(i_s, 2) \wedge eq(i'_s, 2) \triangleright \delta & (14) \end{aligned}$$

Proof. By straightforward process algebraic calculations and Lemma A.3 in the Appendix of [GS01] and the auxiliary definitions given above. \square

The specification of the external behavior of the CABP is a one-datum buffer, which reads a datum at port 1, and sends out this same datum at port 2.

Definition 4.11 The LPE of the external behavior of the CABP is

$$B(d : D, b : \text{Bool}) = \sum_{d' : D} r_1(d') \cdot B(d', \text{F}) \triangleleft b \triangleright \delta + s_2(d) \cdot B(d, \text{T}) \triangleleft \neg b \triangleright \delta.$$

4.2 Verification

Let Ξ abbreviate $D \times \text{Bit} \times \text{Nat} \times \text{Nat} \times D \times \text{Bit} \times \text{Nat} \times D \times \text{Bit} \times \text{Nat} \times \text{Bit} \times \text{Nat}$. Furthermore, let $\xi : \Xi$ denote $(d_s, b_s, i_s, i'_s, d_r, b_r, i_r, d_k, b_k, i_k, b_l, i_l)$.

Invariants \mathcal{I}_1 - \mathcal{I}_5 , \mathcal{I}_7 below were taken from [GS01]. \mathcal{I}_1 - \mathcal{I}_5 describe the range of the data parameters i_s , i'_s , i_k , i_r , and i_l , respectively. \mathcal{I}_6 says that b_s and b_k remain equal until S gets an acknowledgment from AR. \mathcal{I}_7 expresses that each component in Figure 1 either has received information about the datum being transmitted which it must forward, or did not yet receive this information.

Definition 4.12

$$\begin{aligned}
\mathcal{I}_1(\xi) &\equiv eq(i_s, 1) \vee eq(i_s, 2) \\
\mathcal{I}_2(\xi) &\equiv eq(i'_s, 1) \vee eq(i'_s, 2) \\
\mathcal{I}_3(\xi) &\equiv eq(i_k, 1) \vee eq(i_k, 2) \vee eq(i_k, 3) \vee eq(i_k, 4) \\
\mathcal{I}_4(\xi) &\equiv eq(i_r, 1) \vee eq(i_r, 2) \vee eq(i_r, 3) \\
\mathcal{I}_5(\xi) &\equiv eq(i_l, 1) \vee eq(i_l, 2) \vee eq(i_l, 3) \vee eq(i_l, 4) \\
\mathcal{I}_6(\xi) &\equiv \neg eq(i_k, 1) \wedge eq(i_s, 2) \Rightarrow eq(b_s, b_k) \\
\mathcal{I}_7(\xi) &\equiv (eq(i_s, 1) \Rightarrow eq(b_s, inv(b_k)) \wedge eq(b_s, b_r) \wedge eq(d_s, d_k) \\
&\quad \wedge eq(d_s, d_r) \wedge eq(i'_s, 1) \wedge eq(i_r, 1) \wedge eq(i_k, 1)) \\
&\quad \wedge (eq(b_s, b_k) \Rightarrow eq(d_s, d_k)) \\
&\quad \wedge (eq(i_r, 2) \vee eq(i_r, 3) \Rightarrow eq(d_s, d_r) \wedge eq(b_s, b_r) \wedge eq(b_s, b_k)) \\
&\quad \wedge (eq(b_s, inv(b_r)) \Rightarrow eq(d_s, d_r) \wedge eq(b_s, b_k)) \\
&\quad \wedge (eq(b_s, b_l) \Rightarrow eq(b_s, inv(b_r))) \\
&\quad \wedge (eq(i'_s, 2) \Rightarrow eq(b_s, b_l)).
\end{aligned}$$

Lemma 4.13 $\mathcal{I}_1, \mathcal{I}_2, \mathcal{I}_3, \mathcal{I}_4, \mathcal{I}_5, \mathcal{I}_6$ and $\mathcal{I}_4 \wedge \mathcal{I}_7$ are invariants of *Sys*.

Proof. We need to show that the invariants hold in the initial state, and that the invariants are preserved by each of the summands (1) – (14) in the specification of *Sys*.

1. Invariants $\mathcal{I}_1 - \mathcal{I}_5$ are trivial to prove.
2. It is easy to find that invariant \mathcal{I}_6 hold in the initial state.
 - Summands (1): We must show $\mathcal{I}_6 \wedge eq(i_s, 1) \Rightarrow \mathcal{I}_6(d/d_s, 2/i_s)$.
By $eq(i_s, 1)$ and $\mathcal{I}_4 \wedge \mathcal{I}_7$ we have $eq(i_k, 1)$, hence $\mathcal{I}_6(d/d_s, 2/i_s)$.
 - Summands (2): We must show $\mathcal{I}_6 \wedge eq(i_k, 1) \wedge eq(i_s, 2) \Rightarrow \mathcal{I}_6(b_s/b_k, 2/i_k)$.
This is straightforward.
 - Summands (14): We must show $\mathcal{I}_6 \wedge eq(i_s, 2) \wedge eq(i'_s, 2) \Rightarrow \mathcal{I}_6(inv(b_s)/b_s, 1/i_s, 1/i'_s)$.
This is straightforward.
 - The other summands are trivial.
3. To prove $\mathcal{I}_4 \wedge \mathcal{I}_7$, \mathcal{I}_4 holds and it is easy to find that invariant \mathcal{I}_7 hold in the initial state. We divide \mathcal{I}_7 into six parts as follows:

$$\begin{aligned}
\mathcal{I}_{71}(\xi) &\equiv (eq(i_s, 1) \Rightarrow eq(b_s, inv(b_k)) \wedge eq(b_s, b_r) \wedge eq(d_s, d_k) \\
&\quad \wedge eq(d_s, d_r) \wedge eq(i'_s, 1) \wedge eq(i_r, 1) \wedge eq(i_k, 1)) \\
\mathcal{I}_{72}(\xi) &\equiv eq(b_s, b_k) \Rightarrow eq(d_s, d_k) \\
\mathcal{I}_{73}(\xi) &\equiv eq(i_r, 2) \vee eq(i_r, 3) \Rightarrow eq(d_s, d_r) \wedge eq(b_s, b_r) \wedge eq(b_s, b_k) \\
\mathcal{I}_{74}(\xi) &\equiv eq(b_s, inv(b_r)) \Rightarrow eq(d_s, d_r) \wedge eq(b_s, b_k) \\
\mathcal{I}_{75}(\xi) &\equiv eq(b_s, b_l) \Rightarrow eq(b_s, inv(b_r)) \\
\mathcal{I}_{76}(\xi) &\equiv eq(i'_s, 2) \Rightarrow eq(b_s, b_l).
\end{aligned}$$

- Summand (1): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_s, 1) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(d/d_s, 2/i_s)$.
By $eq(i_s, 1)$ and \mathcal{I}_{71} we have $eq(b_s, inv(b_k))$, $eq(i_r, 1)$, $eq(b_s, b_r)$, $eq(i'_s, 1)$, so $\mathcal{I}_{71}((d/d_s, 2/i_s))$, $\mathcal{I}_{72}(d/d_s, 2/i_s)$, $\mathcal{I}_{73}(d/d_s, 2/i_s)$, $\mathcal{I}_{74}(d/d_s, 2/i_s)$, $\mathcal{I}_{76}(d/d_s, 2/i_s)$ hold, and $\mathcal{I}_{75}(d/d_s, 2/i_s)$ is trivial.
- Summand (2): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_s, 2) \wedge eq(i_k, 1) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(d_s/d_k, b_s/b_k, 2/i_k)$.
By $eq(i_s, 2)$, we have $\mathcal{I}_{71}(d_s/d_k, b_s/b_k, 2/i_k)$. $\mathcal{I}_{72}(d_s/d_k, b_s/b_k, 2/i_k)$, $\mathcal{I}_{73}(d_s/d_k, b_s/b_k, 2/i_k)$, $\mathcal{I}_{74}(d_s/d_k, b_s/b_k, 2/i_k)$, $\mathcal{I}_{75}(d_s/d_k, b_s/b_k, 2/i_k)$ and $\mathcal{I}_{76}(d_s/d_k, b_s/b_k, 2/i_k)$ are trivial.

- Summand (4): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_r, 1) \wedge eq(b_r, b_k) \wedge eq(i_k, 3) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(d_k/d_r, 2/i_r, 1/i_k)$. Assume $eq(i_s, 1)$, by \mathcal{I}_{71} , it follows that $eq(b_s, inv(b_k)) \wedge eq(b_s, b_r)$. Hence $eq(b_r, inv(b_k))$, this contradicts the condition $eq(b_r, b_k)$. By \mathcal{I}_1 , we have $eq(i_s, 2)$, hence $\mathcal{I}_{71}(d_k/d_r, 2/i_r, 1/i_k)$. To prove $\mathcal{I}_{73}(d_k/d_r, 2/i_r, 1/i_k)$, we have to show that $eq(d_s, d_r) \wedge eq(b_s, b_r) \wedge eq(b_s, b_k)$. Assume $eq(b_s, inv(b_r))$, by \mathcal{I}_{74} it follows that $eq(b_s, b_k)$, hence $eq(b_k, inv(b_r))$, which contradicts with the condition $eq(b_r, b_k)$. So we have $eq(b_s, b_r)$ and $eq(b_s, b_k)$. By \mathcal{I}_{72} , we have $eq(d_s, d_k)$, hence $\mathcal{I}_{73}(d_k/d_r, 2/i_r, 1/i_k)$. $\mathcal{I}_{72}(d_k/d_r, 2/i_r, 1/i_k)$, $\mathcal{I}_{74}(d_k/d_r, 2/i_r, 1/i_k)$, $\mathcal{I}_{75}(d_k/d_r, 2/i_r, 1/i_k)$ and $\mathcal{I}_{76}(d_k/d_r, 2/i_r, 1/i_k)$ are trivial.
- Summand (5): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_r, 1) \wedge eq(b_r, inv(b_k)) \wedge eq(i_k, 3) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(1/i_k)$. Assume $eq(i_s, 1)$, by \mathcal{I}_{71} , it follows that $eq(i_k, 1)$, which contradicts the condition $eq(i_k, 3)$. By \mathcal{I}_1 we have $eq(i_s, 2)$, hence $\mathcal{I}_{71}(1/i_k)$. $\mathcal{I}_{72}(1/i_k)$, $\mathcal{I}_{73}(1/i_k)$, $\mathcal{I}_{74}(1/i_k)$, $\mathcal{I}_{75}(1/i_k)$ and $\mathcal{I}_{76}(1/i_k)$ are trivial.
- Summand (8): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_r, 3) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(inv(b_r)/b_r, 1/i_r)$. Assume $eq(i_s, 1)$, by \mathcal{I}_{71} , we have $eq(i_r, 1)$, which contradicts the condition $eq(i_r, 3)$. By \mathcal{I}_1 we have $eq(i_s, 2)$, hence $\mathcal{I}_{71}(inv(b_r)/b_r, 1/i_r)$. By $eq(i_r, 3)$ and \mathcal{I}_{73} , it follows that $eq(b_s, b_r) \wedge eq(d_s, d_r) \wedge eq(b_s, b_k)$, so $\mathcal{I}_{74}(inv(b_r)/b_r, 1/i_r)$. Assume $eq(b_s, b_l)$, we have $eq(b_s, inv(b_r))$ by \mathcal{I}_{75} . This contradicts $eq(b_s, b_r)$. So $\neg eq(b_s, b_l)$, hence $\mathcal{I}_{74}(inv(b_r)/b_r, 1/i_r)$. $\mathcal{I}_{72}(inv(b_r)/b_r, 1/i_r)$, $\mathcal{I}_{73}(inv(b_r)/b_r, 1/i_r)$ and $\mathcal{I}_{76}(inv(b_r)/b_r, 1/i_r)$ are trivial.
- Summand (9): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_l, 1) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(inv(b_r)/b_l, 2/i_l)$. If $eq(b_s, b_l)$, by \mathcal{I}_{75} we have $eq(b_s, inv(b_r))$. Hence $\mathcal{I}_{72}(inv(b_r)/b_l, 2/i_l)$. If $eq(i'_s, 2)$, by \mathcal{I}_{76} we have $eq(b_s, b_l)$, and by \mathcal{I}_{75} , we $\mathcal{I}_{76}(inv(b_r)/b_l, 2/i_l)$. The other parts are trivial.
- Summand (11): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i'_s, 1) \wedge eq(b_l, b_s) \wedge eq(i_l, 3) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(1/i_l, 2/i'_s)$. We only need to check \mathcal{I}_{76} . By condition $eq(b_l, b_s)$, $\mathcal{I}_{76}(1/i_l, 2/i'_s)$ is straightforward.
- Summand (14): We must show $\mathcal{I}_4 \wedge \mathcal{I}_7 \wedge eq(i_s, 2) \wedge eq(i'_s, 2) \Rightarrow \mathcal{I}_4 \wedge \mathcal{I}_7(inv(b_s)/b_s, 1/i_s, 1/i'_s)$. To prove $\mathcal{I}_{71}(inv(b_s)/b_s, 1/i_s, 1/i'_s)$, we must show $eq(b_s, inv(b_k)) \wedge eq(b_s, b_r) \wedge eq(d_s, d_k) \wedge eq(d_s, d_r) \wedge eq(i'_s, 1) \wedge eq(i_r, 1)$. Given the conditions, $eq(i_s, 2) \wedge eq(i'_s, 2)$, and the assignments $inv(b_s)/b_s, 1/i_s$ and $1/i'_s$, the formula above reduces to $eq(inv(b_s), inv(b_k)) \wedge eq(b_r, inv(b_s)) \wedge eq(d_s, d_k) \wedge eq(d_s, d_r) \wedge eq(i_r, 1)$, which we must prove, given the invariants and the facts $eq(i_s, 2) \wedge eq(i'_s, 2)$. As $eq(i'_s, 2)$, by \mathcal{I}_{76} we have $eq(b_s, b_l)$, and by \mathcal{I}_{75} , we have $eq(b_s, inv(b_r))$. Hence $eq(b_s, inv(b_r))$. By \mathcal{I}_{74} , it follows that $eq(b_s, b_k) \wedge eq(d_s, d_r)$. As $eq(b_s, b_k)$ and \mathcal{I}_{72} , we have $eq(d_s, d_k)$. Assume that $\neg eq(i_r, 1)$, by \mathcal{I}_4 and \mathcal{I}_{73} , it follows $eq(b_s, b_r)$, which contradicts $eq(b_s, inv(b_r))$. So we have $eq(i_r, 1)$, hence $\mathcal{I}_{71}(inv(b_s)/b_s, 1/i_s, 1/i'_s)$. The other five parts are trivial.
- The other summands are trivial.

⊠

The focus condition for Sys is obtained by taking the disjunction of the summands in the LPE in Definition 4.10 that deal with an external action; these summands are (1) and (7).

Definition 4.14 The focus condition for Sys is

$$FC(\xi) = eq(i_s, 1) \vee eq(i_r, 2).$$

We proceed to prove that each state satisfying the invariants above can reach a focus point by a sequence of internal transitions, carrying labels from $I = \{c_3, c_4, c_5, c_6, c_7, c_8, j\}$.

Lemma 4.15 For each $\xi: \Xi$ with $\mathcal{I}_n(\xi)$ for $n = 1-6$, there is a $\hat{\xi}: \hat{\Xi}$ such that $FC(\hat{\xi})$ and $\xi \xrightarrow{c_1} \dots \xrightarrow{c_n} \hat{\xi}$ with $c_1, \dots, c_n \in I$ in Sys .

Proof. Let $\neg FC(\xi)$; in view of \mathcal{I}_1 and \mathcal{I}_4 this implies $eq(i_s, 2) \wedge (eq(i_r, 1) \vee eq(i_r, 3))$. In case $eq(i_r, 3)$, we can perform $c_5(ac)$ at summand (8) to arrive a state with $eq(i_s, 2) \wedge eq(i_r, 1)$. By \mathcal{I}_3 and summands (2), (3) and (6), we can perform internal actions to reach a state where $eq(i_s, 2) \wedge eq(i_r, 1) \wedge eq(i_k, 3)$. We distinguish two cases.

1. $eq(b_r, b_k)$.

We can perform $c_4(\langle d_k, b_r \rangle)$ at summand (4) to reach a focus point.

2. $eq(b_r, inv(b_k))$.

If $i'_s = 2$, then we can perform $c(ac)$ at summand (14) to reach a focus point, so by \mathcal{I}_2 we can assume that $i'_s = 1$. If $eq(i_l, 3) \wedge eq(b_l, b_s)$, then by performing $c_7(b_l)$ at summand (11) followed by $c_8(ac)$ at summand (14) we can reach a focus point. Otherwise, by \mathcal{I}_5 and summands (10), (12) and (13) we can reach a state where $eq(i_s, 2) \wedge eq(i'_s, 1) \wedge eq(i_r, 1) \wedge eq(i_k, 3) \wedge eq(i_l, 1)$. We can perform $c_6(inv(b_r))$ at summand (9) followed by j at summand (10) to reach a state where $eq(i_s, 2) \wedge eq(i'_s, 1) \wedge eq(i_r, 1) \wedge eq(i_k, 3) \wedge eq(i_l, 3) \wedge eq(b_l, inv(b_r))$. By $eq(b_r, inv(b_k))$, \mathcal{I}_6 and $eq(b_l, inv(b_r))$ we have $eq(b_l, b_s)$. Hence, we can perform $c_7(b_l)$ at summand (11) followed by $c_8(ac)$ at summand (14) to reach a focus point.

□

The state mapping $\phi : \Xi \rightarrow D \times Bool$ is defined by

$$\phi(\xi) = \langle d_s, eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r) \rangle.$$

Note that ϕ is independent of $i'_s, d_r, d_k, b_k, i_k, b_l, i_l$; we write $\phi(d_s, b_s, i_s, b_r, i_r)$.

Theorem 4.16 For all $d:D$ and $b_0, b_1:Bit$,

$$\tau_I(Sys(d, b_0, 1, 1, d, b_0, 1, d, b_1, 1, b_1, 1)) \xleftrightarrow{b} B(d, \top).$$

Proof. It is easy to check that $\bigwedge_{n=1}^7 \mathcal{I}_n(d, b_0, 1, 1, d, b_0, 1, d, b_1, 1, b_1, 1)$.

We obtain the following matching criteria. For class I, we only need to check the summands (4), (8) and (14), as the other nine summands that involve an initial action leave the values of the parameters in $\phi(d_s, b_s, i_s, b_r, i_r)$ unchanged.

1. $eq(i_r, 1) \wedge eq(b_r, b_k) \wedge eq(i_k, 3) \Rightarrow \phi(d_s, b_s, i_s, b_r, i_r) = \phi(d_s, b_s, i_s, b_r, 2/i_r)$
2. $eq(i_r, 3) \Rightarrow \phi(d_s, b_s, i_s, b_r, i_r) = \phi(d_s, b_s, i_s, inv(b_r)/b_r, 1/i_r)$
3. $eq(i_s, 2) \wedge eq(i'_s, 2) \Rightarrow \phi(d_s, b_s, i_s, b_r, i_r) = \phi(d_s, inv(b_s)/b_s, 1/i_s, b_r, i_r)$

The matching criteria for the other four classes are produced by summands (1) and (7). For class II we get:

1. $eq(i_s, 1) \Rightarrow eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r)$
2. $eq(i_r, 2) \Rightarrow \neg(eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r))$

For class III we get:

1. $(eq(i_s, 1) \vee eq(i_r, 2)) \wedge (eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r)) \Rightarrow eq(i_s, 1)$
2. $(eq(i_s, 1) \vee eq(i_r, 2)) \wedge \neg(eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r)) \Rightarrow eq(i_r, 2)$

For class IV we get:

1. $\forall d:D (eq(i_s, 1) \Rightarrow d = d)$

$$2. eq(i_r, 2) \Rightarrow d_r = d_s$$

Finally, for class V we get:

$$1. \forall d:D (eq(i_s, 1) \Rightarrow \phi(d/d_s, b_s, 2/i_s, b_r, i_r) = \langle d, \mathbf{F} \rangle)$$

$$2. eq(i_r, 2) \Rightarrow \phi(d_s, b_s, i_s, b_r, 3/i_r) = \langle d_s, \mathbf{T} \rangle$$

We proceed to prove the matching criteria.

I.1 Let $eq(i_r, 1)$. Then

$$\begin{aligned} \phi(d_s, b_s, i_s, b_r, i_r) &= \langle d_s, eq(i_s, 1) \vee eq(1, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \langle d_s, eq(i_s, 1) \vee eq(2, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \phi(d_s, b_s, i_s, b_r, 2/i_r). \end{aligned}$$

I.2 Let $eq(i_r, 3)$. Then by \mathcal{I}_7 , $eq(b_s, b_r)$. Hence,

$$\begin{aligned} \phi(d_s, b_s, i_s, b_r, i_r) &= \langle d_s, eq(i_s, 1) \vee eq(3, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \langle d_s, \mathbf{T} \rangle \\ &= \langle d_s, eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, inv(b_r)) \rangle \\ &= \phi(d_s, b_s, i_s, inv(b_r)/b_r, 1/i_r). \end{aligned}$$

I.3 Let $eq(i'_s, 2)$. By \mathcal{I}_7 , $eq(b_s, b_i)$, which together with \mathcal{I}_7 yields $eq(b_s, inv(b_r))$. Hence,

$$\begin{aligned} \phi(d_s, b_s, i_s, b_r, i_r) &= \langle d_s, eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \langle d_s, \mathbf{T} \rangle \\ &= \langle d_s, eq(1, 1) \vee eq(i_r, 3) \vee \neg eq(inv(b_s), b_r) \rangle \\ &= \phi(d_s, inv(b_s)/b_s, 1/i_s, b_r, i_r). \end{aligned}$$

II.1 Trivial.

II.2 Let $eq(i_r, 2)$. Then clearly $\neg eq(i_r, 3)$, and by \mathcal{I}_7 , $eq(b_s, b_r)$. Furthermore, according to \mathcal{I}_7 , $eq(i_s, 1) \Rightarrow eq(i_r, 1)$, so $eq(i_r, 2)$ also implies $\neg eq(i_s, 1)$.

III.1 If $\neg eq(i_r, 2)$, then $eq(i_s, 1) \vee eq(i_r, 2)$ implies $eq(i_s, 1)$. If $eq(i_r, 2)$, then by \mathcal{I}_7 , $eq(b_s, b_r)$, so that $eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r)$ implies $eq(i_s, 1)$.

III.2 If $\neg eq(i_s, 1)$, then $eq(i_s, 1) \vee eq(i_r, 2)$ implies $eq(i_r, 2)$. If $eq(i_s, 1)$, then $\neg(eq(i_s, 1) \vee eq(i_r, 3) \vee \neg eq(b_s, b_r))$ is false, so that it implies $eq(i_r, 2)$.

IV.1 Trivial.

IV.2 Let $eq(i_r, 2)$. Then by \mathcal{I}_7 , $eq(d_r, d_s)$.

V.1 Let $eq(i_s, 1)$. Then by \mathcal{I}_7 , $eq(i_r, 1)$ and $eq(b_s, b_r)$. So for any $d:D$,

$$\begin{aligned} \phi(d/d_s, b_s, 2/i_s, b_r, i_r) &= \langle d, eq(2, 1) \vee eq(1, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \langle d, \mathbf{F} \rangle. \end{aligned}$$

V.2

$$\begin{aligned} \phi(d_s, b_s, i_s, b_r, 3/i_r) &= \langle d_s, eq(i_s, 1) \vee eq(3, 3) \vee \neg eq(b_s, b_r) \rangle \\ &= \langle d_s, \mathbf{T} \rangle. \end{aligned}$$

Note that $\phi(d, b_0, 1, b_0, 1) = \langle d, \mathbf{T} \rangle$. So by Theorem 3.4 and Lemma 4.15,

$$\tau_I(Sys(d, b_0, 1, 1, d, b_0, 1, d, b_1, 1, b_1, 1)) \xleftrightarrow{b} B(d, \mathbf{T}).$$

⊠

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