

11

Event Structures and Orthogonal Term Graph Rewriting

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11.1 INTRODUCTION

Several authors have hinted at a connection between transition systems such as are used to describe concurrency, and the reduction sequences that arise in term rewriting and Lambda calculus ([HL91, Sta89], and others). Here we make such a connection precise in the context of term graph rewriting. We construct for every normalizable term graph in an orthogonal term graph rewrite system, an elementary event structure. The events of this structure correspond to the different possible reduction steps that are required to reduce the term graph to normal form. The elements of the associated domain correspond to the possible needed reduction sequences which begin from the given term graph.

Similar connections have been remarked on for orthogonal term rewriting and Lambda calculus, but in those contexts, the possibility of one reduction duplicating another redex makes it more complicated to derive any sort of event structure, and the resulting events are less closely related to physical computations.

In proving the results of this chapter, we found the category-theoretic definition of graph rewriting which we introduced in [Ken91] very useful in avoiding irrelevant technicalities. In particular, it casts further light on the physical meaning of Lévy's equivalence relation on reduction sequences, which definitions in terms of tiling diagrams or permutation of reduction steps fail to do. Concrete definitions such as those of [Sta80, BvEG⁺87] would make these proofs much more complicated, and restrict

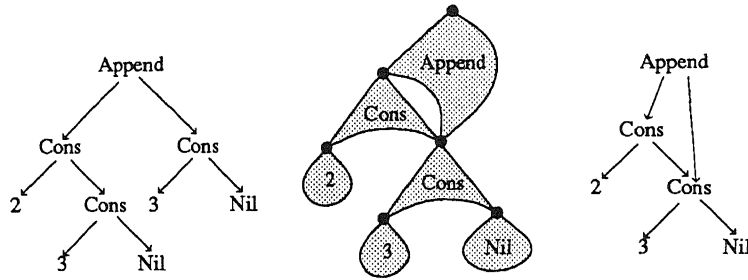


Figure 11.1 Terms and term graphs

them to one particular form of graph, while the more abstract definition may have application to other categories of graphs.

11.2 TERM GRAPH REWRITING

By term graph rewriting we mean, informally, one of the usual methods of implementing rewrite rules such as appear in functional languages such as ML or Miranda. The essential feature is that when a rewrite rule is applied whose right-hand side contains multiple occurrences of a free variable, the corresponding subterm of the expression being evaluated is not duplicated; instead multiple pointers are created to the original copy. The expression is therefore no longer a string or a tree, but a graph of a particular sort: a term graph. It is technically convenient to represent these as hypergraphs—that is, graphs in which each edge may have any positive number of vertexes.

DEFINITION 11.2.1 *Given a set Σ of function symbols, each having some arity (a non-negative integer), a term graph over Σ consists of a tuple (N, E, str) , where N and E are sets, and str (structure) is a function from E to $\Sigma \times N \times N^*$. If $str(e) = (F, n, s)$, then the vertexes of e are n and the members of s . n is the principal vertex of e . (N, E, str) is subject to the following conditions:*

- a. *If $str(e) = (F, n, s)$ then the arity of F is the length of s .*
- b. *Distinct hyperedges have distinct principal vertexes.*

A node is empty if it is not the principal vertex of some hyperedge. A graph is closed if it contains no empty nodes. We use empty nodes to represent free variables. While a separate alphabet of variable symbols is a convenient means of writing term graphs in textual form, it is only a notational device and not a part of the underlying model.

A rooted term graph is a graph together with one of its nodes. It is garbage-free if every node in the graph is accessible from the root. Accessibility is defined thus: n' is accessible from n if either $n = n'$, or $str(n) = (F, n'', s)$, and n' is accessible from n'' or from some member of s .

Figure 11.1 illustrates a term represented as a term graph. On the left is the term shown as a tree. In the middle is a visual representation of the hypergraph, where each of the shaded areas is a hyperedge. On the right is an equivalent representation,

Term graph rewrite rule: $Append(Cons(x, y), z) \rightarrow Cons(x, Append(y, z))$

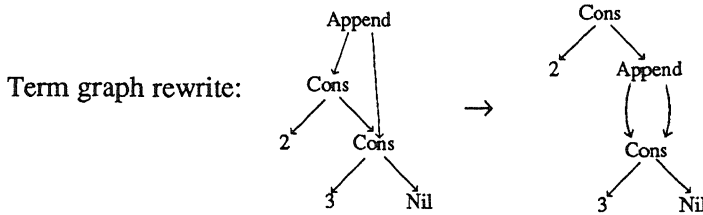


Figure 11.2 Term graph rewrite rule and rewrite

designed for its similarity to the tree picture. Instead of drawing the hyperedges explicitly, the function symbol of each hyperedge is attached to its principal vertex, from which arrows proceed to all the other vertexes of the hyperedge. In this example, the term graph has multiple references to the subgraph $Cons(3, Nil)$ where the term has multiple occurrences of that subterm. However, a hypergraph is allowed to contain distinct isomorphic subgraphs—we do not require maximal sharing as is done e.g. in [HP88].

Formal definitions of term graph rewriting have appeared in [Sta80, BvEG⁺87, Ken91]. We shall here take the notion to be sufficiently intuitive to require no further explanation beyond figures 11.1 and 11.2. However, the particular formalization which we gave in [Ken91] making use of category theory turns out to greatly simplify certain of the concepts and technical arguments which we shall later require, and we shall briefly describe this.

We do not require any advanced concepts of category theory, just the basic notions of category, functor, subobject, limit and colimit (the only limits and colimits we need are pullbacks and pushouts). The hypergraphs we have defined form a category when a notion of homomorphism is given. This notion is the obvious one: a mapping of the nodes and edges of one graph to another which preserves function symbols and connectivity. This category we call \mathcal{J} (for *jungle*, a term coined in [HP88]).

Morphisms preserve structure, but rewrites are intended to change the structure of a graph. We therefore represent rewrite rules not as morphisms of \mathcal{J} , but as the morphisms of a derived category $\wp(\mathcal{J})$, the category of *partial morphisms* of \mathcal{J} .

DEFINITION 11.2.2 *In a category \mathcal{C} , a partial morphism from A to B is a pair of morphisms $A \leftarrow X \rightarrow B$ where $X \rightarrow A$ is a monomorphism. (We may indicate monomorphisms by \hookrightarrow or \hookleftarrow .) More precisely, it is an equivalence class of such pairs. $A \leftarrow X \rightarrow B$ and $A \leftarrow Y \rightarrow B$ are equivalent if there is an isomorphism $X \rightarrow Y$ such that $X \rightarrow Y \rightarrow B = X \rightarrow B$ and $X \rightarrow Y \hookleftarrow A = X \hookleftarrow A$.*

We write a partial morphism from A to B as $A \Rightarrow B$. We shall not always distinguish a partial morphism from a particular representative of the equivalence class.

The partial morphism $A \leftarrow X \rightarrow B$ is total if $X \hookleftarrow A$ is an isomorphism. It is a restriction if $X \rightarrow B$ is an isomorphism.

Assume that \mathcal{C} has the pullback of any pair of arrows of which one is a monomorphism. Then the composition of two partial morphisms $A \leftarrow X \rightarrow B$ and $B \leftarrow Y \rightarrow C$

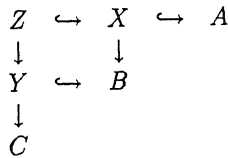


Figure 11.3 Composition of partial morphisms

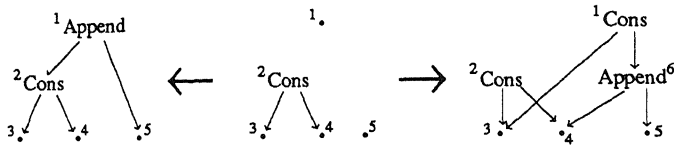


Figure 11.4 A term graph rewrite rule as a partial morphism

is the partial morphism $A \hookrightarrow X \hookrightarrow Z \rightarrow Y \rightarrow C$ given by figure 11.3, in which the square $ZXYB$ is a pullback.

Composition of partial morphisms is associative, and the partial morphisms $A \leftarrow_{id_A} A \rightarrow_{id_A} A$ are identities for it. Thus the objects of \mathcal{C} and partial morphisms form a category, which we denote by $\wp(\mathcal{C})$.

DEFINITION 11.2.3 A term graph rewrite rule is a partial morphism $L \hookrightarrow X \rightarrow R$ of \mathcal{J} , such that X is the subgraph of L obtained by omitting the root hyperedge (but retaining all the nodes), and such that every empty node of R is in the range of $X \rightarrow R$. A redex of this rule in a closed graph G is a total morphism from L to G . The hyperedges of G in the range of this morphism are pattern-matched by the redex. The pre-reduct of this redex is the graph H obtained as the pushout of $L \Rightarrow R$ and $L \Rightarrow G$. One may show that this graph always exists (although $\wp(\mathcal{J})$ does not have all pushouts).

Figure 11.2 exhibited a term graph rewrite by the rule $Append(Cons(x, y), z) \rightarrow Cons(x, Append(y, z))$. Figure 11.4 displays the formulation of this rule as a partial morphism of \mathcal{J} . The attached numbers indicate the actions of the morphisms on nodes. Notice how empty nodes represent variables—we do not need a separate set of variable symbols.

Our definition of rewriting as a pushout of partial morphisms is not yet complete, hence the name pre-reduct. It omits the notion of garbage collection. To define this we must introduce the notion of a rooted graph.

DEFINITION 11.2.4 A rooted graph is a (total) morphism $\bullet \rightarrow G$, where \bullet is the graph with one node and no edges. It is garbage-free if every node of G is accessible from the node which is the image of the morphism $\bullet \rightarrow G$. The result of garbage-collecting this rooted graph, $GC(\bullet \rightarrow G)$, is a garbage-free rooted graph $\bullet \rightarrow G'$ such that there is a monomorphism $G' \rightarrow G$ such that $\bullet \rightarrow G' \rightarrow G = \bullet \rightarrow G$, and such that G' is the largest subgraph of G for which this is so. (Category theorists may note that this

$$\begin{array}{ccccc}
 & & L & \Rightarrow & R \\
 & & \Downarrow & & \Downarrow \\
 \bullet & \rightarrow & G & \Rightarrow & H \Rightarrow H'
 \end{array}$$

Figure 11.5 Reduction step

$$\begin{array}{ccccccccccc}
 & & L_0 & \Rightarrow & R_0 & & L_1 & \Rightarrow & R_1 & & \\
 & & \Downarrow & & \Downarrow & & \Downarrow & & \Downarrow & & \\
 \bullet & \rightarrow & G_0 & \Rightarrow & G'_0 & \Rightarrow & G_1 & \Rightarrow & G'_1 & \Rightarrow & G_2 \dots \Rightarrow G_n
 \end{array}$$

Figure 11.6 Reduction sequence

amounts to an adjoint to the inclusion of the category of garbage-free rooted graphs in the category of rooted graphs, the latter being the comma category $\bullet \downarrow \mathcal{J}$.)

Note that when G is closed, $GC(\bullet \rightarrow G) = \bullet \rightarrow G'$ where G' is the unique closed subgraph of G for which $\bullet \rightarrow G'$ is garbage-free.

In \mathcal{J} , $\bullet \rightarrow G$ factors through G' . In $\wp(\mathcal{J})$, we can also factor $\bullet \rightarrow G'$ through $\bullet \rightarrow G$ as $\bullet \rightarrow G \Rightarrow G'$, where $G \Rightarrow G'$ is the restriction morphism $G \leftarrow G' \cong G'$.

DEFINITION 11.2.5 *Given a rooted graph $\bullet \rightarrow G$, the result of reducing a redex $L \Rightarrow G$ of a rule $L \Rightarrow R$ is depicted in figure 11.5. The square $LRGH$ is a pushout, performing a pre-reduction as above. $\bullet \rightarrow G \Rightarrow H$ is in fact total, since the domain of $G \Rightarrow H$ includes all the nodes of G . We can therefore apply garbage-collection to it and obtain a rooted graph $\bullet \rightarrow H \Rightarrow H'$. This is the reduct of the redex.*

This defines a single reduction step. A reduction sequence can be constructed by stringing successive reductions together as in figure 11.6. One important feature of this definition of rewriting is that it gives additional information besides the final graph: it also gives a partial morphism from the initial graph to the final graph which has a concrete and intuitive meaning. Let the morphism be $G_0 \leftarrow X \rightarrow G_n$. Consider X as a subgraph of G_0 . Then the nodes and hyperedges of G_0 outside X are those which the sequence erases. Hyperedges in X are preserved by the reduction. Nodes which are empty in X but nonempty in G are changed. Other nodes of X are preserved. The nodes and hyperedges of G_n outside the range of $X \rightarrow G_n$ are created by the reduction.

DEFINITION 11.2.6 *Let there be given two distinct redexes $L_1 \Rightarrow G$ and $L_2 \Rightarrow G$ of rules $L_1 \Rightarrow R_1$ and $L_2 \Rightarrow R_2$. They are disjoint if there is no hyperedge of G which is erased by reduction of one redex but pattern-matched by the other.*

A rule system is orthogonal if no graph contains non-disjoint redexes.

In the remainder of the chapter we restrict our attention to orthogonal systems.

11.3 REDUCTION GRAPHS

Besides the graphs with whose rewriting we are concerned, we deal with another sort of graph.

A *reduction graph* of a term graph t is a rooted directed graph labeled as follows. Each node is labeled with a term graph. For each arc, the term labeling its source is reducible in one step to the term labeling its target. The root of the graph is labeled with t , and all nodes are accessible from the root. It is possible for different nodes to be labeled with the same term.

We can consider several different reduction graphs of a term graph t . First, there is the *reduction tree* of t , denoted $RT(t)$. As its name implies, it is a tree. The out-arcs of each node are in 1–1 correspondence with the set of all the redexes of the term graph labeling that node. These two properties uniquely identify $RT(t)$. Its nodes are in 1–1 correspondence with the set of finite reduction sequences starting from t .

The *minimal reduction graph* of t , $MG(t)$, is obtained from $RT(t)$ by identifying together all nodes bearing the same label, and corresponding out-arcs of such nodes.

A third reduction graph concerns us here: the *Lévy graph* of t , or $LG(t)$. This stands midway between $RT(t)$ and $MG(t)$. Like $MG(t)$, it is obtained from $RT(t)$ via an equivalence relation on reduction sequences, but one finer than that associated with $MG(t)$: the relation of *Lévy-equivalence*. This is defined in the next section.

11.4 LÉVY-EQUIVALENCE

Consider the rewrite rule $A(B) \rightarrow C$ and the graph $D(A(x : B), A(x))$. The graph contains two redexes. There is an obvious sense in which we can reduce them both, in either order, and it is clear that the result is the same: $D(C, C)$. This notion is formalized as Lévy-equivalence. Lévy originally defined this for Lambda calculus [Lév78, Lév80], but it applies to orthogonal rewriting in general [HL91]. For term graph rewriting, it is rather simpler than for Lambda calculus or term rewriting, and our categorical formulation of rewriting makes it simple to define. The construction is performed by the following lemmas.

LEMMA 11.4.1 *In figure 11.7, let the squares $L_1R_1GG_1$ and $L_2R_2GG_2$ be pre-reductions of redexes $r_1 : L_1 \Rightarrow G$ and $r_2 : L_2 \Rightarrow G$. Then the pushout of $G \Rightarrow G_1$ and $G \Rightarrow G_2$ exists as the third square of that figure, and the rectangles $L_1R_1GG_2$ and $L_2R_2GG_1$ represent pre-rewrites of G_2 and G_1 respectively to G' .*

PROOF. By orthogonality, the node $r_1(\text{root}(L_1))$ of G cannot be the image by r_2 of any nonempty node of L_2 . The domain of $G \Rightarrow G_1$ consists of every node of G except $r_1(\text{root}(L_1))$. Therefore $L_2 \Rightarrow G \Rightarrow G_1$ must be total. Its pushout with $L_2 \Rightarrow R_2$ therefore exists, and by standard facts about pushouts in general, it must be given by adjoining the pushout $L_2R_2GG_2$ with a pushout GG_1G_2G . Similarly, $L_1 \Rightarrow G \Rightarrow G_2$ is total, and the rectangle $L_1R_1G_2H$ must also be a pushout. \square

LEMMA 11.4.2 *Let $\bullet \rightarrow G$ pre-reduce to $\bullet \rightarrow H$. Then $GC(\bullet \rightarrow G)$ reduces to $GC(\bullet \rightarrow H)$ by either a single step reduction or an empty reduction. (“Garbage collection commutes with reduction.”)*

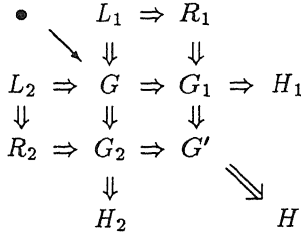


Figure 11.7

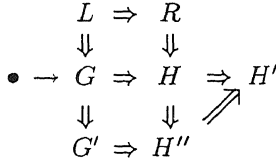


Figure 11.8

PROOF. It is enough to show this for a single step pre-reduction of $\bullet \rightarrow G$ to $\bullet \rightarrow H$. Let $GC(\bullet \rightarrow G) = \bullet \rightarrow G \Rightarrow G'$ and $GC(\bullet \rightarrow H) = \bullet \rightarrow G \Rightarrow H'$. If the root of L is not in the domain of $L \Rightarrow G \Rightarrow G'$, then the node changed by the reduction of $L \Rightarrow G$, and all nodes added by that reduction, are garbage in H . Thus $G' = H'$, and $GC(\bullet \rightarrow G')$ reduces to $GC(\bullet \rightarrow H')$ by the empty reduction.

Otherwise, take the pushout H'' of $G \Rightarrow H$ and $G \Rightarrow G'$ (see figure 11.8). H'' is also the pushout of $L \Rightarrow R$ and $L \Rightarrow G \Rightarrow G'$. Since $G \Rightarrow G'$ is a restriction morphism, so is $H \Rightarrow H''$. Therefore $H \Rightarrow H'$ factors through $H \Rightarrow H''$. Therefore $GC(\bullet \rightarrow G \Rightarrow G' \Rightarrow H'') = GC(\bullet \rightarrow G \Rightarrow H)$. \square

LEMMA 11.4.3 *In figure 11.7, let G pre-reduce to G_1 and G_2 , and reduce to H_1 and H_2 , by reduction of the distinct redexes $r_1 : L_1 \Rightarrow G$ and $r_2 : L_2 \Rightarrow G$. Then H_1 and H_2 both reduce to the same graph H , which is obtained by garbage-collecting the pushout G' of $G \Rightarrow G_1$ and $G \Rightarrow G_2$.*

PROOF. From the preceding lemmas. \square

The reductions of H_1 and H_2 to H constructed by this lemma are called the *projections* of r_2 over r_1 and of r_1 over r_2 respectively, denoted by r_2/r_1 and r_1/r_2 . Projection of single reduction steps is extended to reduction sequences by the equations: $r/(r' \cdot s) = (r/r')/s$ and $(r \cdot s)/s' = (r/s') \cdot (s/(s'/r))$, where r and r' are single steps and s and s' are reduction sequences.

DEFINITION 11.4.4 *On finite reduction sequences, Lévy equivalence is the equivalence relation \cong_L generated by the following axioms:*

- a. $r \cdot (r'/r) \cong_L r' \cdot (r/r')$
- b. $s \cong_L s' \Rightarrow s \cdot s'' \cong_L s' \cdot s'' \wedge s'' \cdot s \cong_L s'' \cdot s'$.

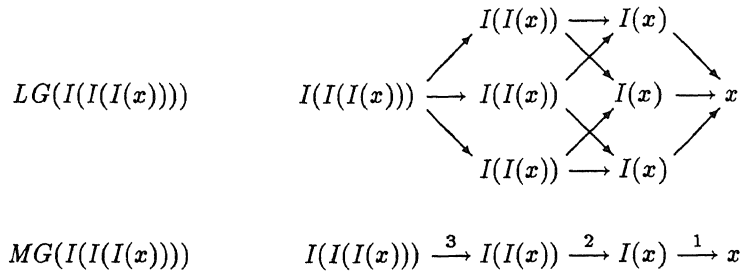


Figure 11.9 Minimal and Lévy graphs of $I(I(I(x)))$

THEOREM 11.4.5 [Lév78, Lév80] *The above definition is equivalent to: $s \cong_L s'$ if and only if s/s' and s'/s are both the empty sequence.*

THEOREM 11.4.6 *Lévy-equivalent sequences determine the same partial morphism.*

PROOF. It is sufficient to verify this for the cases where the two sequences are related by one of the conditions of definition 11.4.4, which is immediate from the preceding lemmas. □

Note that the converse does not hold. By the above theorem, Lévy-equivalent sequences do the same thing to each node and hyperedge of their common initial graph, but in addition, they also do the same thing to each node or hyperedge they create. Sequences determining the same partial morphism need not do the latter.

Here is a simple example where the Lévy reduction graph of a term graph differs from its minimal reduction graph. Take the rule $I(x) \rightarrow x$ and the term $I(I(I(x)))$. The minimal and Lévy reduction graphs of this term are illustrated in figure 11.9. The numbers on the edges of the minimal graph indicate their multiplicity.

11.5 EVENT STRUCTURES

We now define event structures. These were invented by Winskel [NPW81, Win80] to give a semantics for Petri-nets.

There are several types of event structure. We will only require the simplest of them.

DEFINITION 11.5.1 *An elementary event structure is a finite or countable set E and a partial ordering \leq of E . \leq is called the causality relation.*

A left closed subset of E is a subset X such that $e \leq e' \wedge e' \in X \Rightarrow e \in X$.

$\mathcal{L}(E)$ denotes the set of left closed subsets of E , ordered by inclusion.

THEOREM 11.5.2 [Win80] *$\mathcal{L}(E)$ is a prime algebraic complete lattice.*

The intuition behind these definitions is that E is the set of events that can happen in the course of a computation. The partial ordering is a relation of dependency or causality: when $e < e'$, then e' cannot happen unless e has already happened. The members of $\mathcal{L}(E)$ thus represent possible computational states: a state is the set of events which have happened so far.

11.6 EVENT STRUCTURES FOR ORTHOGONAL TERM GRAPH REWRITING

11.6.1 Pre-events

The intuition underlying the following construction is that given a redex r of a graph G , and a reduction sequence $s : G \rightarrow G'$, if r/s is nonempty then it is in some sense the same piece of work as r , deferred to a later time.

DEFINITION 11.6.1 *A pre-event of a term graph G is a pair (s, r) , where $s : G \rightarrow H$ is a reduction sequence and r is a redex of H . $\text{Pre}(G)$ is the set of all pre-events of G . For any reduction sequence s , the pre-events of s , denoted $\text{Pre}(s)$, are the events (s', r) such that $s' \cdot r$ is an initial segment of s .*

DEFINITION 11.6.2 *Two pre-events are equivalent if they can be proved so by the following axioms:*

- a. $(s, r) \cong (s', r)$ if $s \cong_L s'$.
- b. $(s, r) \cong (s \cdot s', r/s')$ if r/s' exists.

We now arrive at a theorem which is fundamental to the interpretation of term graphs as event structures. The rest of this section is devoted to its proof.

THEOREM 11.6.3 *No two distinct pre-events of a reduction sequence are equivalent.*

PROOF. We proceed by establishing properties of pre-events which are of minimal length in their equivalence class. Equivalent minimal pre-events are found to satisfy a much stronger equivalence relation. From this the theorem will follow.

DEFINITION 11.6.4 *A pre-event (s, r) is minimal if there is no $(s', r') \cong (s, r)$ with $|s'| < |s|$. A pre-event (s, r) is irredundant if every pre-event of s contributes to a later pre-event of $s \cdot r$. (Equivalently, if every pre-event of s is needed for the pre-event (s, r) .)*

LEMMA 11.6.5 *Every minimal pre-event is irredundant.*

PROOF. Let (s, r) be a counterexample of minimal length. Let r_0 and r_1 be the first two steps of $s \cdot r$. By minimality of the counterexample, r_0 does not contribute to any later step. Therefore r_0 does not create r_1 . Let $r_1 = r_2/r_0$. If $r_0 = s$ and $r_1 = r$, then $((), r_2) \cong (r_0, r_1)$, contradicting minimality. Otherwise, $s = r_0 \cdot r_1 \cdot s' \cong_L r_2 \cdot (r_0/r_1) \cdot s'$. If r_0/r_1 is empty, then $(r_2 \cdot s', r) \cong (s, r)$ and $r_2 \cdot s'$ is shorter than s , contradicting minimality. Finally, if r_0/r_1 is nonempty, it does not contribute to any step of $s' \cdot r$, and so $((r_0/r_1) \cdot s', r)$ is a shorter counterexample. \square

We can elaborate the above proof into an algorithm for transforming any pre-event (s, r) into an equivalent irredundant pre-event.

ALGORITHM 11.6.6 Firstly, note that if s is empty, (s, r) is irredundant.

For nonempty s , we will deal with each step of s , from the last backwards. At each stage, we will have transformed (s, r) into an equivalent pre-event $(s_0 \cdot r_0 \cdot s_1, r_1)$, where (s_1, r_1) is minimal. Initially, $s_0 \cdot r_0 = s$ and s_1 is empty (making (s_1, r_1) irredundant).

If $((), r_0)$ contributes to some later step of $r_0 \cdot s_1 \cdot r_1$, then $(r_0 \cdot s_1, r_1)$ is irredundant. Otherwise, we need the following lemma, proved below.

LEMMA 11.6.7 *If $(\langle \rangle, r)$ does not contribute to any later pre-event of a sequence $r \cdot s$, then there is a sequence s' such that $s = s'/r$ and $|s| = |s'|$.*

Applying this lemma to the situation where $(\langle \rangle, r_0)$ does not contribute to any later step of $r_0 \cdot s_1 \cdot r_1$, we find that there is an $s_2 \cdot r_2$ such that $s_1 \cdot r_1 = (s_2 \cdot r_2)/r_0$ and $|s_2 \cdot r_2| = |s_1 \cdot r_1|$. This implies that projection over r_0 does not erase any step of $s_2 \cdot r_2$, and that in particular $r_1 = r_2/(r_0/s_2)$. Therefore $(r_0 \cdot s_1, r_1) \cong (s_2, r_2)$. Since (s_1, r_1) is irredundant, by Theorem 11.6.5 (s_2, r_2) is also. Furthermore $(s_0 \cdot r_0 \cdot s_1, r_1) \cong (s_0 \cdot s_2, r_2)$.

By continuing in this way, we process each member of s , obtaining in the end an equivalent irredundant pre-event (s', r') .

PROOF OF LEMMA. If s is empty this is trivial. Otherwise $s = r' \cdot s'$, where r does not create r' . Then $r' = r''/r$ for some r'' , and in the sequence $(r/r'') \cdot s'$, if r/r'' is nonempty, it does not contribute to any later pre-event of the sequence. s' is shorter than s , so by induction we may assume that there is an s'' such that $s' = s''/(r/r'')$ and $|s'| = |s''|$. Then $s = (r''/r) \cdot (s''/(r/r'')) = (r'' \cdot s'')/r$ and $|s| = |r'' \cdot s''|$ \square

As a corollary, the above construction also provides us with a reduction sequence s'' such that $s' \cdot r' \cdot s''/s \cdot r$ is empty. s'' consists, roughly speaking, of the parts of s that were not needed for (s, r) .

DEFINITION 11.6.8 *The number of steps of s which are needed for (s, r) is the needed length of (s, r) .*

LEMMA 11.6.9 *If the minimization algorithm transforms (s, r) into (s', r') , then $|s'|$ is the needed length of (s, r) .*

PROOF. Clear from the construction. \square

DEFINITION 11.6.10 *Two pre-events (s, r) and (s', r') are strongly equivalent if $s \cong_L s'$ and $r = r'$.*

LEMMA 11.6.11 *If $(s, r) \cong (s', r')$ then the needed lengths of the two pre-events are equal. Furthermore, the respective results of applying the minimization algorithm to both are strongly equivalent.*

PROOF. It is sufficient to prove this when (s, r) and (s', r') are related by either of the axioms of Definition 11.4.4. Since the second part of the lemma implies the first, it is sufficient to prove only the second part.

For the second axiom, it is clear that the minimization algorithm will produce identical results given either (s, r) or $(s \cdot s', r/s')$.

For the first axiom, it is sufficient to take the case where $r = r'$ and $s = s_0 \cdot r_0 \cdot (r_1/r_0) \cdot s_1 \cong_L s_0 \cdot r_1 \cdot (r_0/r_1) \cdot s_1 = s'$. For s to be distinct from s' , at least one of r_1/r_0 and r_0/r_1 must be nonempty. Assume r_1/r_0 is nonempty.

Applying the minimization algorithm, we may assume without loss of generality that (s_1, r) is minimal. We must show that the results of applying the algorithm on the one hand to r_1/r_0 and then r_0 , and on the other hand to r_0/r_1 (if nonempty) and r_1 , have the same length, and that this equality of length is preserved when we apply the algorithm to s_0 . This may be shown by induction on $|s_1|$. When s_1 is nonempty, there

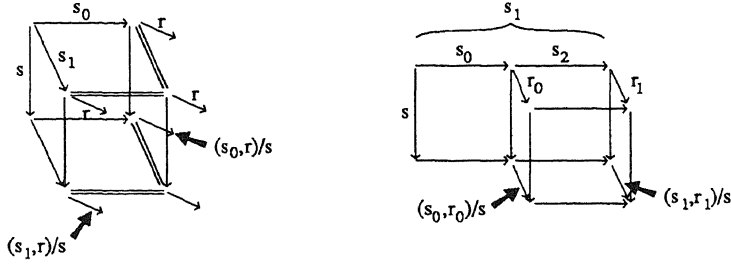


Figure 11.10

is a routine analysis of cases, according to which, if either, of r_0 and r_1/r_0 contributes to the first step of s_1 . We omit the details. \square

LEMMA 11.6.12 a. An irredundant pre-event is minimal.

b. Distinct redexes of the same term, considered as pre-events with empty history, are not equivalent.

PROOF. (i) If $(s, r) \cong (s', r')$ and (s, r) is irredundant, then from Lemmas 11.6.9 and 11.6.11, $|s \cdot r| \leq |s' \cdot r'|$, i.e. (s, r) is minimal.

(ii) Distinct redexes are minimal pre-events, yet not strongly equivalent. Hence (ii) follows from Lemma 11.6.11. \square

LEMMA 11.6.13 If $(s_0, r_0) \cong (s_1, r_1)$ and both $(s_0, r_0)/s$ and $(s_1, r_1)/s$ exist, then they are equivalent.

PROOF. It is sufficient to show this for the two cases of the definition of equivalence of pre-events. Each of these in turn follows from the Cube Lemma; see figure 11.10. \square

Finally, we complete the proof of Theorem 11.6.3.

$$\begin{aligned}
 (s_0, r_0) &\cong (s_0 \cdot r_0 \cdot s_1, r_1) \\
 \Rightarrow (s_0, r_0)/s_0 &\cong (s_0 \cdot r_0 \cdot s_1, r_1)/s_0 && \text{(Lemma 11.6.13)} \\
 \Rightarrow (\langle \rangle, r_0) &\cong (r_0 \cdot s_1, r_1)
 \end{aligned}$$

Apply the minimization algorithm to $(r_0 \cdot s_1, r_1)$. This must yield a pre-event of the form $(\langle \rangle, r_2)$, such that $r_1 = r_2/(r_0 \cdot s_1)$. But $(\langle \rangle, r_0)$ and $(\langle \rangle, r_2)$ must be strongly equivalent, hence $r_0 = r_2$, and $r_2/(r_0 \cdot s_1)$ cannot exist. \square

Informally, this theorem means that it is not possible for a reduction sequence to do the same piece of work twice. This theorem also shows the distinction between term graph rewriting and term rewriting. *Mutatis mutandis*, it is false for the latter, because of the possibility that reduction of one redex can make multiple copies of another, which may all be later reduced. By the definitions we have given, the reductions of each of these copies, considered as pre-events, would be equivalent. Reducing more than one of them would give a reduction sequence containing two or more equivalent pre-events, defeating the construction of an event structure, in which an event is something which can only happen once.

11.6.2 Events

DEFINITION 11.6.14 *An event of G is an equivalence class of pre-events of G . $Ev(G)$ is the set of events of G . $Ev(s)$ is the set of events which are represented by the members of $Pre(s)$.*

The pre-events of a graph have an immediate computational interpretation as the possible steps which may be executed by a reduction machine evaluating the graph. Theorem 11.6.3 implies that the more abstract events may be interpreted in the same way. Furthermore, if we consider a machine capable of executing distinct redexes concurrently, without necessarily any definite total ordering of its reductions beyond that implied by causality, then events precisely correspond to the steps which may be made by such a machine.

$Ev(s)$ does not quite describe the work done by s , since it is possible for some steps of s to erase parts of the graph in which some previous steps were performed, making those steps unnecessary.

DEFINITION 11.6.15 *An event e of a graph G is needed if $e \in Ev(s)$ for every reduction s of G to normal form. $Ev^0(G)$ is the set of needed events of G . A needed reduction sequence is one, all of whose events are needed. $LG^0(G)$ is the subgraph of $LG(G)$ obtained by restricting to needed reduction steps.*

$Ev^0(s)$ is the description we seek, as shown by the next theorem.

THEOREM 11.6.16 *$s \cong_L s'$ if and only if $Ev^0(s) = Ev^0(s')$.*

PROOF. The forwards implication is immediate from the definition of Ev^0 .

For the converse, suppose $s_1 \not\cong s_2$. Choose sequences s_1 and s_2 Lévy-equivalent to s_1 and s_2 respectively and of minimal length. At least one of s_1/s_2 and s_2/s_1 must be nonempty. Supposing it is the first, consider the sequence $s_2 \cdot (s_1/s_2)$. By Theorem 11.6.3, no step in the second segment can be equivalent to any step of s_2 . But every step of the second segment is equivalent to a step of s_1 . Therefore $Ev(s'_1) \neq Ev(s'_2)$. But $Ev^0(s_1) = Ev^0(s_1) = Ev(s_1)$, and similarly for s_2 , hence the theorem. \square

11.6.3 The event structure of needed events

We now show that the notion of one redex creating another gives rise to a partial ordering of $Ev(t)$.

DEFINITION 11.6.17 *For e and e' in $Ev(G)$, define $e \leq e'$ if for every reduction sequence s starting from G , if $e' \in Ev(s)$ then $e \in Ev(s)$. (It is immediate that this is indeed a partial order.)*

The partial order has a concrete meaning.

DEFINITION 11.6.18 *In a sequence $s \cdot r \cdot s' \cdot r'$, the pre-event (s, r) contributes to the pre-event $(s \cdot r \cdot s', r')$ if there is a node n which is either the root of the contractum of r or is created by r , which is preserved by s' , and such that n/s' is matched by r' .*

(s, r) is needed for $(s \cdot r \cdot s', r')$ if it contributes to $(s \cdot r \cdot s', r')$ or if it contributes to a later step of $s \cdot r \cdot s'$ which is needed for $(s \cdot r \cdot s', r')$.

THEOREM 11.6.19 $e_0 < e_1$ if and only if there is a sequence of the form $s_0 \cdot r_0 \cdot s_1 \cdot r_1$ such that (s_0, r_0) is needed for $(s_0 \cdot r_0 \cdot s_1, r_1)$, and these two pre-events represent e_0 and e_1 respectively.

PROOF. Let $e_0 < e_1$. By the definition of the ordering, there must be a sequence $s_0 \cdot r_0 \cdot s_1 \cdot r_1$ where (s_0, r_0) and $(s_0 \cdot r_0 \cdot s_1, r_1)$ represent e_0 and e_1 respectively. If (s_0, r_0) were not needed for $(s_0 \cdot r_0 \cdot s_1, r_1)$, then applying the minimization algorithm to $(s_0 \cdot r_0 \cdot s_1, r_1)$ would begin by transforming it to a form $(s_0 \cdot r_0 \cdot s_1, r_1)$ with (s_1, r_1) a minimal pre-event, and then eliminate r_0 . The final result would be a pre-event (s_2, r_2) equivalent to $(s_0 \cdot r_0 \cdot s_1, r_1)$, and in which $Ev(s_2)$ is a subset of $Ev(s_0 \cdot r_0 \cdot s_1)$ not containing e_0 (since by Theorem 6.3, no other pre-event of $s_0 \cdot r_0 \cdot s_1$ can be equivalent to (s_0, r_0)). But this contradicts $e_0 < e_1$.

For the converse, suppose we have a sequence $s_0 \cdot r_0 \cdot s_1 \cdot r_1$ of the stated form. To establish the ordering of the events, we must show that for any pre-event (s_2, r_2) equivalent to $(s_0 \cdot r_0 \cdot s_1, r_1)$, s_2 must contain a pre-event equivalent to (s_0, r_0) . It is sufficient to do this for the cases where (s_2, r_2) is related to $(s_0 \cdot r_0 \cdot s_1, r_1)$ by one of the axioms for equivalence.

Axiom (i): $s_2 \cong_L s_0 \cdot r_0 \cdot s_1$. We may assume that s_2 and $s_0 \cdot r_0 \cdot s_1$ are related by an application of part (1) of Definition 2.2.1 to a part of $s_0 \cdot r_0 \cdot s_1$. If this part does not include r_0 , then s_2 will have a pre-event (s'_0, r_0) equivalent to (s_0, r_0) . Otherwise, there are two cases.

(a) There exist s_3 and $r_3 \neq r_0$ such that $s_2 = s_0 \cdot r_3 \cdot (r_0/r_3) \cdot s_3$, $s_1 = (r_0/r_3) \cdot s_3$, and $r_2 = r_1$. If r_0/r_3 is nonempty, then $(s_0 \cdot r_3, (r_0/r_3)) \cong (s_0, r_0)$. Otherwise, r_3 erases r_0 . But this implies that r_3/r_0 erases every node which r_0 changes or creates. Therefore r_0 cannot contribute to any step of $s_1 \cdot r_1$ later than r_3/r_0 . It cannot contribute to r_3/r_0 either, since this is a residual of a redex existing before r_0 . This contradicts the hypothesis that (s_0, r_0) is needed for $(s_0 \cdot r_0 \cdot s_1, r_1)$.

(b) There exist s_3, r_3 and r_4 such that $s_0 = s_3 \cdot r_3$, $r_0 = r_4/r_3$, and $s_2 = s_3 \cdot r_4 \cdot (r_3/r_4) \cdot s_1$. Then $(s_3, r_4) \cong (s_0, r_0)$.

Axiom (ii): There are three subcases.

(a) There exists r_3 such that $s_2 = s_0 \cdot r_0 \cdot s_1 \cdot r_3$ and $r_2 = r_1/r_3$. Then s_2 contains the pre-event (s_0, r_0) .

(b) There exist s_3 and r_3 such that $s_1 = s_3 \cdot r_3$, $r_1 = r_2/r_3$, and $s_2 = s_0 \cdot r_0 \cdot s_3$. Again, s_2 contains the pre-event (s_0, r_0) .

(c) s_1 is empty, $s_2 = s_0$, and there exists r_2 such that $r_1 = r_2/r_0$. But this implies that $(s_0, r_0) \cong (s_0 \cdot r_0 \cdot s_1, r_1)$, contradicting Theorem 6.3. \square

Finally, we have the required event structure and its associated configuration domain.

THEOREM 11.6.20 $Ev^0(G)$ with the partial ordering inherited from $Ev(G)$ (of which it is a lower section) is an elementary event structure. Its associated domain of configurations is isomorphic to $LG^0(G)$. The resulting partial ordering of $LG^0(G)$ is identical to the ordering defined by Huet and Lévy [HL91]: $s \leq s'$ if and only if s/s' is empty.

PROOF. It is immediate that $Ev^0(G)$ is a lower section of $Ev(G)$. Nodes of $LG^0(G)$ are in 1–1 correspondence with Lévy-equivalence classes of needed reduction sequences, which by Theorem 6.6 are in 1–1 correspondence with $Ev^0(G)$.

The configuration domain of $Ev^0(G)$ is the set of lower sections, ordered by the subset relation. Let s and s' be needed reduction sequences. If s/s' is empty, then $s' \cong_L s \cdot (s'/s)$, therefore $Ev^0(s) \subseteq Ev^0(s')$. Conversely, suppose $Ev^0(s) \subseteq Ev^0(s')$. In the sequence $s' \cdot (s/s')$, every step in the s/s' segment is equivalent to some step of s , hence by hypothesis to some step of s' . But by Theorem 6.3 there can be no such step. Therefore s/s' is empty, and $s \leq s'$. \square

Thus a Lévy-equivalence class of needed reductions starting from G is equivalent to a lower section of the set of needed events of G .

11.7 RELATED WORK AND FURTHER DEVELOPMENTS

In [Sta89], Stark defines a notion of “concurrent transition system”. This takes as basic a notion of an abstract residual operation on abstract transitions. However, his primary concern is the study of process networks. His paper only mentions in passing the possibility of constructing event structures from concurrent transition systems, and that orthogonal term rewriting and Lambda calculus reduction can give rise to examples of such structures. However, finding such structures in these contexts requires taking the basic transitions to be not ordinary reductions, but complete developments, which amounts to considering term graph reduction without the name. The construction—which is the purpose of this chapter—is still non-trivial.

We expect that the construction of event structures can also be applied in the presence of infinite graphs and transfinite rewriting, as set out in [KKSdV90]. The set $Ev^0(t)$ is generalized to the set of *Böhm-needed* redexes of t —those redexes which must be reduced in any reduction of t which obtains every part of its Böhm tree (a concept borrowed from Lambda calculus).

To extend this work to non-orthogonal systems, we would have to deal with the possibility of conflicts among events. While event structures with a notion of conflict are well known, all such structures in the literature depend on a conflict relation which is symmetric: if event e_1 conflicts with e_2 , then e_2 conflicts with e_1 . This is in general not the case for conflicts among redexes. Consider the rules $F(A) \rightarrow B$, $A \rightarrow C$. There is a conflict between these rules. The graph $F(A)$ may be reduced either to B or to $F(C)$. In this case, the conflict between the two redexes is symmetric. If one reduces either redex, the other no longer exists. However, consider the graph $D(x : F(y : A), y)$. This again contains two conflicting redexes. Reducing the redex at y breaks the redex at x . But reducing the redex at x gives the graph $D(x : B, y : A)$, in which the redex at y is still present. A type of event structure based on an asymmetric conflict relation is therefore required.

11.8 CONCLUSION

For any normalizable term graph t in an orthogonal term graph rewrite system, the essentially different pieces of work which are required in the evaluation of t to normal form form an elementary event structure. The partial ordering embodies the relation of one redex contributing to another. Redexes can be reduced in any order compatible with the dependency relation.

The associated state domain is the set of Lévy-equivalence classes of needed reduction sequences starting from t . The top element corresponds to the reduction of t to normal form. The height of the partial ordering of $Ev^0(t)$ implies a lower bound on the time required to reach the normal form by reduction, and the width implies an upper bound on the amount of useful parallelism that can be employed.

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